

Q1.

- a. Shift a register value right (4pts)
- Shift in 0's (1pt)
- Shift by other register value (1pt)

* No points if text is copied (plagiarized) from ARM ARM (unless they cite the source)

b. 0000 0000 1111 1111 m m m d d d
 i: 0000 0110 1110 1010 ⇒ 0x06EA (2pts)

ii: 0100 0000 10 m m m d d d
 0100 0000 1010 1010 ⇒ 0x40AA (2pts)

c. R11 cannot be encoded (Rd is on 3 bits for encoding T1) (1pt)

LSLS.W R11, R3, #13 (1pt)

11 00 1010 0105 1111 0000 d d d d 00 m m m m ⇒ 0xEA5F3B43 (1pt)

0101 0011 1011 0100 0011

Q2.

a.

BASE	00	01	02	03
...04	00	00	00	00
00	18	BA	0C	FE
FC	67	45	23	01
F8	00	00	00	00

2pts endianness
 3pts correct location in memory

b.

```

mov r1, #0x100    r1 = 0x100
movw r2, #136     r2 = 0x88
movt r2, #136     r2 = 0x00080088
strh r2, [r1], #-3 [100] = 0x88, [101] = 0x00 ; r1 = 94
str r2, [r1, #2]   r1 = 102, [102] = 88, [103] = 00, [104] = 08, [105] = 00
    
```

0FE	0
0FF	0
100	0x88
101	0
102	0x88
103	0
104	0x08
105	0

5 pts if correct. 2pts if you can follow their work and they were close.

Q3. Two solutions... THIS QUESTION IS HARD TO GRADE! PLEASE TAKE YOUR TIME!
 -5 pts if no comments. -2 pts per mistake until @.

callee-save: must save any regs used > r3.

```

main:
    push {r4, r5, r6, lr} ; callee-save regs
    mov r4, #1 ; set initial values?
    mov r5, #0
    mov r6, #2
loop:
    cmp r5, #9
    bge done
    add r4, r5
    mov r0, r4 ; arg in r0?
    bl print ; must bl not just b
    add r5, r6
    b loop
done:
    mov r0, r4 ; return in r0?
    pop {r4, r5, r6, pc} ; must restore regs > r3
    
```

caller-save: print call will overwrite LR, must save

```

main:
    push {lr} ; save LR
    mov r0, #1
    mov r1, #0
loop:
    cmp r1, #9
    bge done
    add r0, r1
    push {r0, r1}
    bl print
    pop {r0, r1}
    add r1, #2
    b loop
done:
    pop {pc}
    
```

Annotations for Q3:

- main: must have label main
- loop: correct branch
- arg in r0? → mov r0, r4
- must bl not just b
- done: return in r0?
- must restore regs > r3
- pop is PC, NOT LR
- caller-save: must save any regs used > r3.
- main: push {lr} ; save LR
- loop: cmp r1, #9 ; must cmp before branch (unless careful use of subs + offset, UNLIKELY!)
- push {r0, r1}
- bl print
- pop {r0, r1}
- add r1, #2
- b loop
- done: pop {pc}
- print fn call corrupts r0-r3, must save/restore if they are used
- don't forget to increment i
- stride = 2 ← inefficient! is ok
- set initial values?

Q4.

```
uint32_t * x = (uint32_t *) 0xA5001008;
*x = *x - 5;
```

create constant 2pts
 assign as pointer 2pts
 ↳ minor syntax issue -1pt
 load pointer value 2pts
 do subtraction 2pts
 store result to pointer 2pts

Q5.

```
mean: add r0, r0, r1
      add r0, r0, r2
      add r0, r0, r3
      lsr r0, r0, #2
      bx lr
```

-2 pts if missing label "mean"
 -2 pts if missing bx lr
 -2 pts per instruction > 5 instructions
 +5 pts extra credit if < 5 instructions (must be correct)
 -5 pts if does n't calculate mean correctly but is close
 -10 pts if not close
 (cannot go below 0).