High-level software pipelining in LLVM

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Motivation

Concepts & Course Review

• Software Pipelining

- Loop scheduling
- Increasing the instruction level parallelism
- II: Initiation Interval
- **MaxLive**: Maximum number of simultaneously live values at any cycle

• Goal

- **Higher throughput** (smaller II, smaller stage count)
- Lower register requirements (smaller MaxLive)
- The task of generating an optimal resource-constrained schedule for loops is known to be NP-hard
- Heuristics

Drawbacks of Existing Scheduling Techniques

- Huge Computational Cost
 - Aggressive Schedulings
 - Integer Linear Programming
- Not Considering Critical Path
 - Hypernode Reduction Modulo Scheduling (HRMS)*

- Suboptimal Reduction
 - Stage Scheduling*
- Ejection of Previously Scheduled Operations
 - Slack Scheduling*

*All three schedulings use heuristic technique

Swing Modulo Scheduling (SMS)

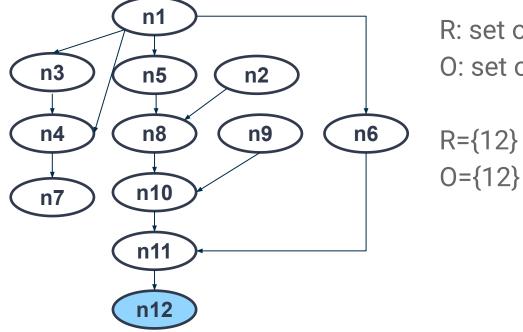
Node Ordering

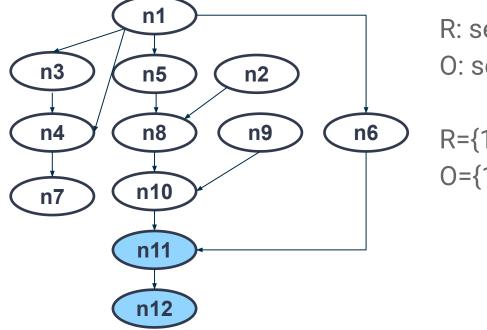
Target

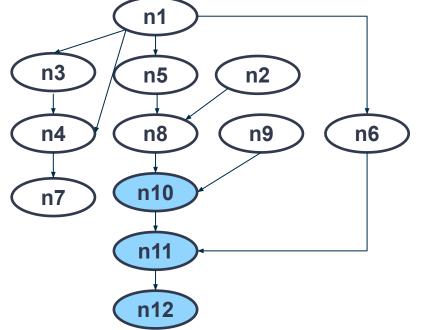
- Give priority to operations in the most critical paths.
- Try to reduce MaxLive

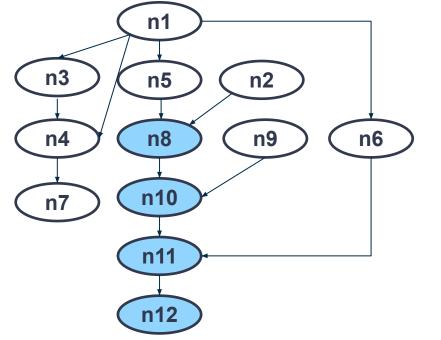
Traversing Algorithm

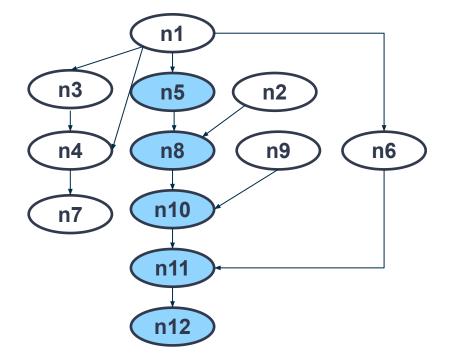
- Starts by the node at the bottom of the most critical path and moves upwards, visiting all the ancestors
- Once all the ancestors have been visited all the descendants of the already ordered nodes are visited but now moving downwards.
- Successive upwards and downwards sweeps

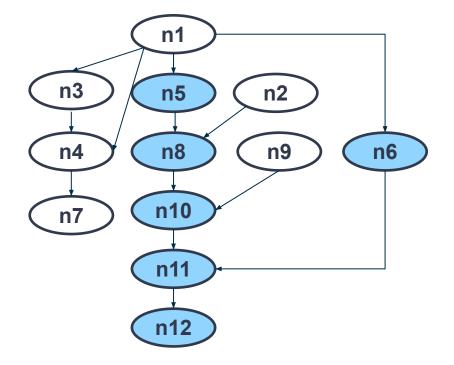


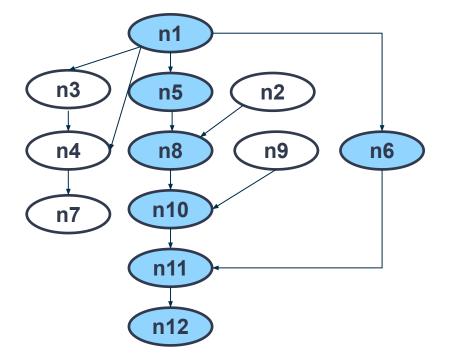


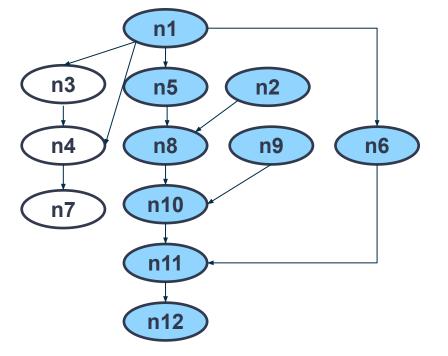


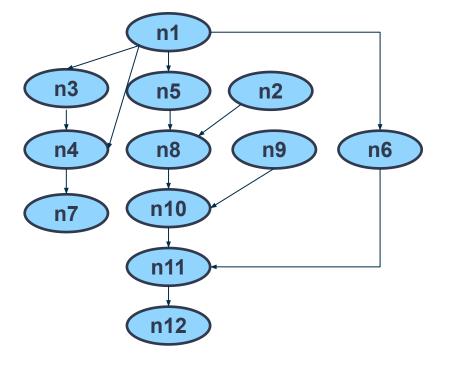










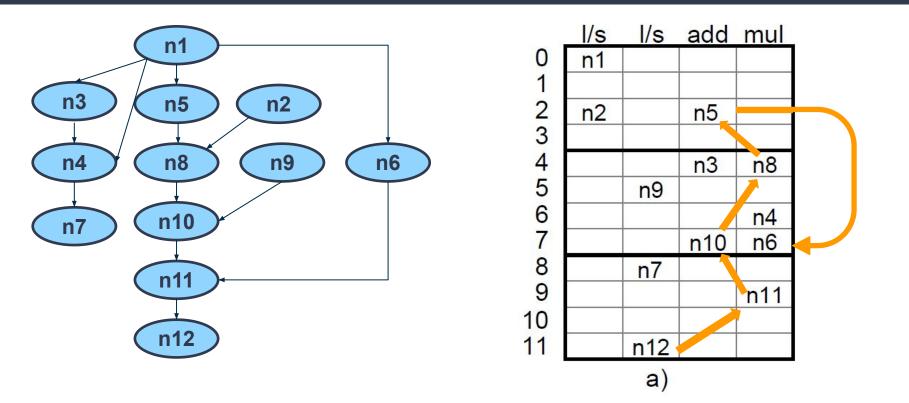


Scheduling

Tries to schedule the operations **as close as possible** to the neighbors that have already been scheduled. If an operation **u** has:

- **Only predecessors** in the partial schedule, then **u** is scheduled as soon as possible.
- **Only successors** in the partial schedule, then **u** is scheduled as late as possible.
- Both predecessors and successors, rare case, only occurs once for each recurrence.

Scheduling, 0={12, 11, 10, 8, 5, 6, 1, 2, 9, 3, 4, 7}

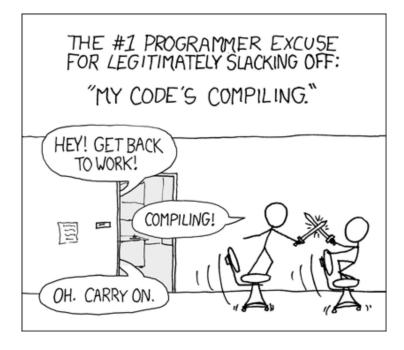


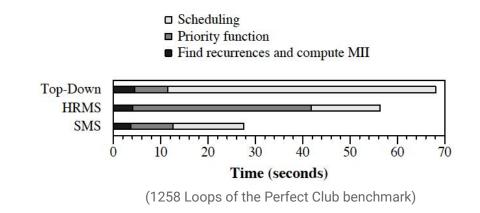
Experiments and Results

Benchmark

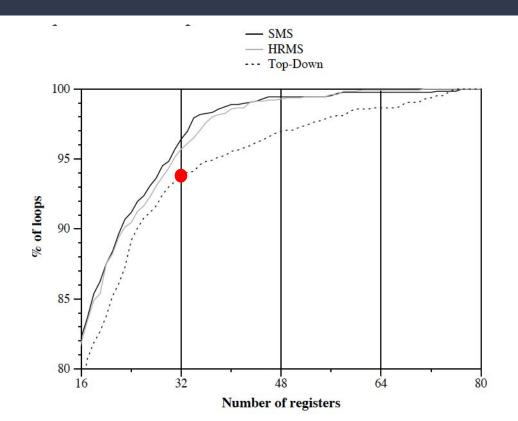
- C++ (LEDA libraries)
- Perfect Club benchmark suite without subroutine calls or conditional exits.
- Compared with HRMS(Hypernode reduction modulo scheduling) and Top-Down scheduling.

Compilation Speed





Register Usage



Comparison with Optimal Solution

Program	Loop	Optimal			SMS			L		L			1		L
		Π	SC	Regs.	П	SC	Regs.	fppp	1	20	2	2	20	2	2
<u> </u>	1		Man Bole of		1	Second Sectors		Livermore	1	3	3	6	3	4	7
Spice	1	1	3	3	1	3	3		5	3	2	3	3	2	3
	2	6	3	5	6	3	6		0.008		1000000	1	100 NV	4	21000
	3	6	1	2	6	1	2		23	9	2	10	9	2	11
	4	11	2	8	11	2	8	Linpack	1	2	3	5	2	3	5
	5	2	2	1	2	2	1	Whetstone	1	17	1	5	17	1	5
	6	2	12	15	2	12	15		2	6	5	6	6	5	6
	7	3	7	15	3	7	15		3	5	1	4	5	1	4
	8	3	2	5	3	2	5		1	4	1	1	4	1	1
	10	3	2	2	3	2	3		2	4	1	2	4	1	2
Doduc	1	20	2	5	20	2	7		4	4	1	4	4	1	4
	3	20	2	3	20	2	4		8	4	1	8	4	1	8
	7	2	18	18	2	18	18	8 b		ve -			100	95 - 88	

Strength and Weakness

Strength

Weakness

- Produced schedules are very close to the optimal scheduling
- Low computational cost

- Required a slight higher registers and stages than optimal schedule
- Missing opportunities for further instruction level parallelism by only handling simple basic block loops

Conclusions

Conclusion

- SMS produces near optimal schedules while requiring a very low compilation time.
- Outperforms other heuristics approaches, which is measured by the attained initiation interval, register requirements and stage count.
- Compares against the optimal solution which was obtained using an integer linear programming approach.
- SMS obtains the initiation interval in all the cases and its schedules requiring only 5% more registers and a 1% higher stage count.

