
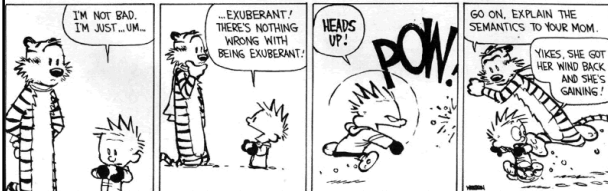


Axiomatic Semantics III  
 ---  
 The Verification Crusade

IM NOT BAD. IM JUST...UM...  
 ...EXUBERANT! THERE'S NOTHING WRONG WITH BEING EXUBERANT!  
 HEADS UP!  
 GO ON, EXPLAIN THE SEMANTICS TO YOUR MOM.  
 YIKES, SHE GOT HER WIND BACK AND SHE'S GAINING!

### Wei Hu Memorial Homework Award

- Many turned in HW3 code like this:  
 let rec matches re s = match re with  
 | Star(r) -> union (singleton s)  
 (matches (Concat(r, Star(r))) s)
- Which is a direct translation of:  
 $R[r^*]s = \{s\} \cup R[r^*]s$   
 or, equivalently:  
 $R[r^*]s = \{s\} \cup \{y \mid \exists x \in R[r]s \wedge y \in R[r^*]x\}$
- Why doesn't this work?

### Forward VC Gen Intuition

- Consider the sequence of assignments  
 $x_1 := e_1; x_2 := e_2$
- The  $VC(c, B) = [e_1/x_1]([e_2/x_2]B)$   
 $= [e_1/x_1, e_2[e_1/x_1]/x_2] B$
- We can compute the substitution in a forward way using symbolic execution (aka symbolic evaluation)
  - Keep a symbolic state that maps variables to expressions
  - Initially,  $\Sigma_0 = \{\}$
  - After  $x_1 := e_1$ ,  $\Sigma_1 = \{x_1 \rightarrow e_1\}$
  - After  $x_2 := e_2$ ,  $\Sigma_2 = \{x_1 \rightarrow e_1, x_2 \rightarrow e_2[e_1/x_1]\}$
  - Note that we have applied  $\Sigma_1$  as a substitution to right-hand side of assignment  $x_2 := e_2$

### Simple Assembly Language

- Consider the language of instructions:  
 $I ::= x := e \mid f() \mid \text{if } e \text{ goto } L \mid \text{goto } L \mid L: \mid \text{return} \mid \text{inv } e$
- The "inv e" instruction is an annotation
  - Says boolean expression e holds at that point
- Each function f() comes with  $Pre_f$  and  $Post_f$  annotations (pre- and post-conditions)
- New Notation (yay!):  $I_k$  is the instruction at address k

### Symex States

- We set up a symbolic execution state:  
 $\Sigma : \text{Var} \rightarrow \text{SymbolicExpressions}$   
 $\Sigma(x)$  = the symbolic value of x in state  $\Sigma$   
 $\Sigma[x:=e]$  = a new state in which x's value is e
- We use states as substitutions:  
 $\Sigma(e)$  - obtained from e by replacing x with  $\Sigma(x)$
- Much like the opsem so far ...

### Symex Invariants

- The symbolic executor tracks invariants passed
- A new part of symex state:  $Inv \subseteq \{1...n\}$
- If  $k \in Inv$  then  $I_k$  is an invariant instruction that we have already executed
- Basic idea: execute an inv instruction only twice:
  - The first time it is encountered
  - Once more time around an arbitrary iteration

## Symex Rules

- Define a VC function as an interpreter:

VC : Address  $\times$  SymbolicState  $\times$  InvariantState  $\rightarrow$  Assertion

VC(k, $\Sigma$ , Inv) =	$VC(L, \Sigma, Inv)$	if $I_k = \text{goto } L$
	$e \Rightarrow VC(L, \Sigma, Inv) \wedge$ $\neg e \Rightarrow VC(k+1, \Sigma, Inv)$	if $I_k = \text{if } e \text{ goto } L$
	$VC(k+1, \Sigma[x:=\Sigma(e)], Inv)$	if $I_k = x := e$
	$\Sigma(\text{Post}_{\text{current-function}})$	if $I_k = \text{return}$
	$\Sigma(\text{Pre}_f) \wedge$ $\forall a_1 \dots a_m. \Sigma'(\text{Post}_f) \Rightarrow$ $VC(k+1, \Sigma', Inv)$ (where $y_1, \dots, y_m$ are modified by $f$ ) and $a_1, \dots, a_m$ are fresh parameters and $\Sigma' = \Sigma[y_1 := a_1, \dots, y_m := a_m]$	if $I_k = f()$

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## Symex Invariants (2a)

Two cases when seeing an invariant instruction:

- We see the invariant for the first time

- $I_k = \text{inv } e$
- $k \notin \text{Inv}$  (= "not in the set of invariants we've seen")
- Let  $\{y_1, \dots, y_m\}$  = the variables that could be modified on a path from the invariant back to itself
- Let  $a_1, \dots, a_m$  be fresh new symbolic parameters

VC(k,  $\Sigma$ , Inv) =

$\Sigma(e) \wedge \forall a_1 \dots a_m. \Sigma'(e) \Rightarrow VC(k+1, \Sigma', \text{Inv} \cup \{k\})$

with  $\Sigma' = \Sigma[y_1 := a_1, \dots, y_m := a_m]$

(like a function call)

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## Symex Invariants (2b)

- We see the invariant for the second time

- $I_k = \text{inv } E$
- $k \in \text{Inv}$

VC(k,  $\Sigma$ , Inv) =  $\Sigma(e)$

(like a function return)

- Some tools take a more simplistic approach
  - Do not require invariants
  - Iterate through the loop a fixed number of times
  - PREfix, versions of ESC (DEC/Compaq/HP SRC)
  - Sacrifice completeness for usability

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## Where Are We?

- Axiomatic Semantics:** the meaning of a program is what is true after it executes
- Hoare Triples:**  $\{A\} c \{B\}$
- Weakest Precondition:**  $\{WP(c,B)\} c \{B\}$
- Verification Condition:**  $A \Rightarrow VC(c,B) \Rightarrow WP(c,b)$ 
  - Requires **Loop Invariants**
  - Backward VC works for structured programs
  - Forward VC (**Symbolic Exec**) works for assembly
  - Here we are today ...

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## Today's Cunning Plan

- Symbolic Execution & Forward VCGen
- Handling **Exponential** Blowup
  - Invariants
  - Dropping Paths
- VCGen For Exceptions (double trouble)
- VCGen For Memory (McCarthyism)
- VCGen For Structures (have a field day)
- VCGen For **"Dictator For Life"**

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## Symex Summary

- Let  $x_1, \dots, x_n$  be all the variables and  $a_1, \dots, a_n$  fresh parameters
- Let  $\Sigma_0$  be the state  $[x_1 := a_1, \dots, x_n := a_n]$
- Let  $\emptyset$  be the empty Inv set
- For all functions  $f$  in your program, prove:
 
$$\forall a_1 \dots a_n. \Sigma_0(\text{Pre}_f) \Rightarrow VC(f_{\text{entry}}, \Sigma_0, \emptyset)$$
- If you start the program by invoking any  $f$  in a state that satisfies  $\text{Pre}_f$ , then the program will execute such that
  - At all "inv  $e$ " the  $e$  holds, and
  - If the function returns then  $\text{Post}_f$  holds
- Can be proved w.r.t. a real interpreter (operational semantics)
- Or via a proof technique called co-induction (or, [assume-guarantee](#))

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## Forward VCGen Example

- Consider the program
  - Precondition:  $x \leq 0$
  - Loop:  $\text{inv } x \leq 6$ 
    - if  $x > 5$  goto End
    - $x := x + 1$
    - goto Loop
  - End: return Postcondition:  $x = 6$

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## Forward VCGen Example (2)

$$\forall x. x \leq 0 \Rightarrow x \leq 6 \wedge \forall x'. (x' \leq 6 \Rightarrow x' > 5 \Rightarrow x' = 6 \wedge x' \leq 5 \Rightarrow x' + 1 \leq 6)$$

- VC contains both proof obligations and assumptions about the control flow

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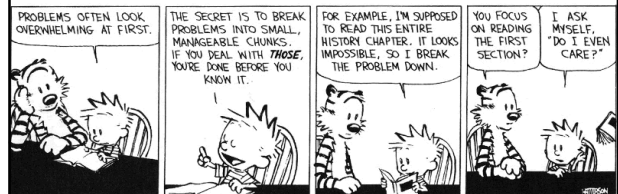
## VCs Can Be Large

- Consider the sequence of conditionals  
(if  $x < 0$  then  $x := -x$ ); (if  $x \leq 3$  then  $x += 3$ )
  - With the postcondition  $P(x)$
- The VC is
  - $x < 0 \wedge -x \leq 3 \Rightarrow P(-x+3) \wedge$
  - $x < 0 \wedge -x > 3 \Rightarrow P(-x) \wedge$
  - $x \geq 0 \wedge x \leq 3 \Rightarrow P(x+3) \wedge$
  - $x \geq 0 \wedge x > 3 \Rightarrow P(x)$
- There is one conjunct for each path
  - $\Rightarrow$  exponential number of paths!
  - Conjuncts for infeasible paths have un-satisfiable guards!
- Try with  $P(x) = x \geq 3$

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## VCs Can Be Exponential

- VCs are exponential in the size of the source because they attempt relative completeness:
  - Perhaps the correctness of the program must be argued independently for each path
- Unlikely that the programmer wrote a program by considering an exponential number of cases
  - But possible. Any examples? Any solutions?



## VCs Can Be Exponential

- VCs are exponential in the size of the source because they attempt relative completeness:
  - Perhaps the correctness of the program must be argued independently for each path
- Standard Solutions:
  - Allow invariants even in straight-line code
  - And thus do not consider all paths independently!

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## Invariants in Straight-Line Code

- Purpose: modularize the verification task
- Add the command "after c establish Inv"
  - Same semantics as c (Inv is only for VC purposes)
- $$\text{VC}(\text{after } c \text{ establish Inv, } P) =_{\text{def}} \text{VC}(c, \text{Inv}) \wedge \forall x_i. \text{Inv} \Rightarrow P$$
  - where  $x_i$  are the ModifiedVars(c)
- Use when c contains many paths
  - after if  $x < 0$  then  $x := -x$  establish  $x \geq 0$ ;
  - if  $x \leq 3$  then  $x += 3$  {  $P(x)$  }
- VC is now:
  - $(x < 0 \Rightarrow -x \geq 0) \wedge (x \geq 0 \Rightarrow x \geq 0) \wedge$
  - $\forall x. x \geq 0 \Rightarrow (x \leq 3 \Rightarrow P(x+3) \wedge x > 3 \Rightarrow P(x))$

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## Dropping Paths

- In absence of annotations, we can drop some paths
- VC(if E then  $c_1$  else  $c_2$ , P) = choose one of
  - $E \Rightarrow VC(c_1, P) \wedge \neg E \Rightarrow VC(c_2, P)$  (drop no paths)
  - $E \Rightarrow VC(c_1, P)$  (drops "else" path!)
  - $\neg E \Rightarrow VC(c_2, P)$  (drops "then" path!)
- We sacrifice soundness!** (we are now unsound)
  - No more guarantees
  - Possibly still a good debugging aid
- Remarks:
  - A recent trend is to sacrifice soundness to increase usability (e.g., Metal, ESP, even ESC)
  - The PREFIX tool considers only 50 non-cyclic paths through a function (almost at random)

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## VCGen for Exceptions

- We extend the source language with exceptions without arguments (cf. HW2):
  - throw throws an exception
  - try  $c_1$  catch  $c_2$  executes  $c_2$  if  $c_1$  throws
- Problem:
  - We have **non-local transfer of control**
  - What is VC(throw, P) ?

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  - What is VC(throw, P) ?
- Standard Solution: use 2 postconditions
  - One for normal termination
  - One for exceptional termination

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## VCGen for Exceptions (2)

- VC( $c$ , P, Q) is a precondition that makes  $c$  either not terminate, or terminate normally with P **or throw an exception with Q**
- Rules
  - $VC(\text{skip}, P, Q) = P$
  - $VC(c_1; c_2, P, Q) = VC(c_1, VC(c_2, P, Q), Q)$
  - $VC(\text{throw}, P, Q) = Q$
  - $VC(\text{try } c_1 \text{ catch } c_2, P, Q) = VC(c_1, P, VC(c_2, P, Q))$
  - $VC(\text{try } c_1 \text{ finally } c_2, P, Q) = ?$

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## VCGen Finally

- Given these:
  - $VC(c_1; c_2, P, Q) = VC(c_1, VC(c_2, P, Q), Q)$
  - $VC(\text{try } c_1 \text{ catch } c_2, P, Q) = VC(c_1, P, VC(c_2, P, Q))$
- Finally is somewhat like "if":
  - $VC(\text{try } c_1 \text{ finally } c_2, P, Q) = VC(c_1, VC(c_2, P, Q), \text{true}) \wedge VC(c_1, \text{true}, VC(c_2, Q, Q))$
- Which reduces to:
  - $VC(c_1, VC(c_2, P, Q), VC(c_2, Q, Q))$

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## Hoare Rules and the Heap

- When is the following Hoare triple valid?
  - $\{ A \} *x := 5 \{ *x + *y = 10 \}$
- A **should be** " $*y = 5$  or  $x = y$ "
- The Hoare rule for assignment would give us:
  - $[5/*x]( *x + *y = 10) = 5 + *y = 10 = *y = 5$  (we lost one case)
- Why didn't this work?**



## Handling The Heap

- We do not yet have a way to talk about **memory (the heap, pointers)** in assertions
- Model the **state of memory as a symbolic mapping** from addresses to values:
  - If  $A$  denotes an address and  $M$  is a memory state then:
  - $\text{sel}(M, A)$  denotes the contents of the memory cell
  - $\text{upd}(M, A, V)$  denotes a new memory state obtained from  $M$  by writing  $V$  at address  $A$

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## More on Memory

- We allow variables to range over memory states
  - We can quantify over all possible memory states
- Use the special pseudo-variable  $\mu$  (mu) in assertions to refer to the current memory
- Example:

$$\forall i. i \geq 0 \wedge i < 5 \Rightarrow \text{sel}(\mu, A + i) > 0$$

says that entries 0..4 in array  $A$  are positive

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## Hoare Rules: Side-Effects

- To model writes we use memory expressions
  - A memory write changes the value of memory

$$\frac{}{\{ B[\text{upd}(\mu, A, E)/\mu] \} * A := E \{ B \}}$$

- **Important technique: treat memory as a whole**
- And **reason later about memory expressions** with inference rules such as ([McCarthy Axioms](#), -'67):

$$\text{sel}(\text{upd}(M, A_1, V), A_2) = \begin{cases} V & \text{if } A_1 = A_2 \\ \text{sel}(M, A_2) & \text{if } A_1 \neq A_2 \end{cases}$$

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## Memory Aliasing

- Consider again:  $\{ A \} * x := 5 \{ *x + *y = 10 \}$
- We obtain:

$$\begin{aligned} A &= [\text{upd}(\mu, x, 5)/\mu] (*x + *y = 10) \\ &= [\text{upd}(\mu, x, 5)/\mu] (\text{sel}(\mu, x) + \text{sel}(\mu, y) = 10) \\ (1) &= \text{sel}(\text{upd}(\mu, x, 5), x) + \text{sel}(\text{upd}(\mu, x, 5), y) = 10 \\ &= 5 + \text{sel}(\text{upd}(\mu, x, 5), y) = 10 \\ &= \text{if } x = y \text{ then } 5 + 5 = 10 \text{ else } 5 + \text{sel}(\mu, y) = 10 \\ (2) &= x = y \text{ or } *y = 5 \end{aligned}$$

- Up to (1) is **theorem generation**
- From (1) to (2) is **theorem proving**

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## Alternative Handling for Memory

- Reasoning about aliasing can be expensive
  - It is **NP-hard (and/or undecidable)**
- Sometimes completeness is sacrificed with the following (approximate) rule:

$$\text{sel}(\text{upd}(M, A_1, V), A_2) = \begin{cases} V & \text{if } A_1 = (\text{obviously}) A_2 \\ \text{sel}(M, A_2) & \text{if } A_1 \neq (\text{obviously}) A_2 \\ p & \text{otherwise (p is a fresh new parameter)} \end{cases}$$

- The meaning of "obviously" varies:
  - The addresses of two distinct globals are  $\neq$
  - The address of a global and one of a local are  $\neq$
- PREFIX and GCC use such schemes

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## VCGen Overarching Example

- Consider the program
  - Precondition:  $B : \text{bool} \wedge A : \text{array}(\text{bool}, L)$
  - 1:  $I := 0$
  - $R := B$
  - 3:  $\text{inv } I \geq 0 \wedge R : \text{bool}$
  - if  $I \geq L$  goto 9
  - assert  $\text{saferd}(A + I)$
  - $T := *(A + I)$
  - $I := I + 1$
  - $R := T$
  - goto 3
  - 9: return  $R$
  - Postcondition:  $R : \text{bool}$

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## VCGen Overarching Example

- $$\forall A. \forall B. \forall L. \forall \mu$$
- $$B : \text{bool} \wedge A : \text{array}(\text{bool}, L) \Rightarrow$$
- $$0 \geq 0 \wedge B : \text{bool} \wedge$$
- $$\forall I. \forall R. \forall \mu.$$
- $$I \geq 0 \wedge R : \text{bool} \Rightarrow$$
- $$I \geq L \Rightarrow R : \text{bool}$$
- $$\wedge$$
- $$I < L \Rightarrow \text{saferd}(A + I) \wedge$$
- $$I + 1 \geq 0 \wedge$$
- $$\text{sel}(\mu, A + I) : \text{bool}$$
- VC contains both **proof obligations** and assumptions about the control flow

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## Mutable Records - Two Models

- Let  $r : \text{RECORD} \{ f1 : T1; f2 : T2 \} \text{END}$
- For us, records are reference types
- Method 1: one “memory” for **each record**
  - One index constant for each field
  - $r.f1$  is  $\text{sel}(r, f1)$  and  $r.f1 := E$  is  $r := \text{upd}(r, f1, E)$
- Method 2: one “memory” for **each field**
  - The record address is the index
  - $r.f1$  is  $\text{sel}(f1, r)$  and  $r.f1 := E$  is  $f1 := \text{upd}(f1, r, E)$
- **Only works in strongly-typed languages like Java**
  - Fails in C where  $\&r.f2 = \&r + \text{sizeof}(T1)$

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## VC as a “Semantic Checksum”

- Weakest preconditions are an expression of the program’s **semantics**:
  - Two **equivalent programs have logically equivalent WPs**
  - No matter how different their syntax is!
- VC are almost as powerful

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## VC as a “Semantic Checksum” (2)

- Consider the “assembly language” program to the right

```
x := 4
x := x == 5
assert x : bool
x := not x
assert x
```

- High-level type checking is not appropriate here
- The VC is:  $4 == 5 : \text{bool} \wedge \text{not } (4 == 5)$
- No confusion from reuse of  $x$  with different types

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## Invariance of VC Across Optimizations

- VC is so good at abstracting syntactic details that it is **syntactically preserved** by many common optimizations
  - Register allocation, instruction scheduling
  - Common subexp elim, constant and copy propagation
  - Dead code elimination
- We have **identical** VCs whether or not an optimization has been performed
  - Preserves syntactic form, not just semantic meaning!
- This can be used to verify correctness of compiler optimizations (Translation Validation)

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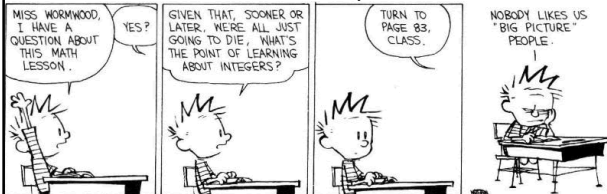
## VC Characterize a Safe Interpreter

- Consider a fictitious “safe” interpreter
  - As it goes along it **performs checks** (e.g. “safe to read from this memory addr”, “this is a null-terminated string”, “I have not already acquired this lock”)
  - Some of these would actually be **hard to implement**
- The VC describes **all** of the checks to be performed
  - Along with their context (assumptions from conditionals)
  - Invariants and pre/postconditions are used to obtain a finite expression (through induction)
- **VC is valid  $\Rightarrow$  interpreter never fails**
  - We enforce same level of “correctness”
  - But better (static + more powerful checks)

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## VC Big Picture

- Verification conditions
  - Capture the semantics of code + specifications
  - Language independent
  - Can be computed backward/forward on structured/unstructured code
  - Make Axiomatic Semantics practical



## Invariants Are Not Easy

- Consider the following code from QuickSort

```
int partition(int *a, int L0, int H0, int pivot) {
  int L = L0, H = H0;
  while(L < H) {
    while(a[L] < pivot) L++;
    while(a[H] > pivot) H--;
    if(L < H) { swap a[L] and a[H] }
  }
  return L
}
```
- Consider verifying only memory safety
- What is the loop invariant for the outer loop?

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## Homework

- Homework 4 Due Thursday
- Read Cousot & Cousot article
- Read Abramski article
- Project Proposal Due In One Week

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