

Notes & Hints For Lab 6

1. When you try to attach the EECS373 library on a CAEN machine, the path will be “k:\f98\eeecs373\Xilinx”.
2. Use a counter component like “CB16CE” to divide down the clock to a frequency that you can use for the 7-segment displays.
3. Never put a gate between the clock source and a flip-flop. If you need to selectively clock a value into a flip-flop (and you will!) use a flip-flop with a “CE” input. This “Clock Enable” input, when HIGH, causes the flip-flop to clock data in. When LOW, the flip-flop will hold its value, regardless of the state of the clock or D inputs.
4. The eight usable LEDs in the bar-graph display should be used to display the value on the DIP switches in binary (i.e. the LEDs should mirror the state of the DIP switches). Although it’s called a “bar graph” display because it’s typically used to display bar graphs, we won’t be using it as such in this lab.