

Characterization of Semilumped CPW Elements for Millimeter-Wave Filter Design

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Abstract—In this paper, two accurate models for interdigital capacitors and shunt inductive stubs in coplanar-waveguide structures are presented and validated over the entire W -band frequency range. Using these models, a novel bandpass filter (BPF) and a miniaturized high-pass filter are designed and fabricated. By inserting interdigital capacitors in BPF resonators, an out-of-band transmission null is introduced, which improves rejection level up to 17 dB over standard designs of similar filters. A high-pass filter is also designed, using semilumped-element models in order to miniaturize the filter structure. It is shown that a fifth-order high-pass filter can be built with a maximum dimension of less than $\lambda_g/3$. Great agreement between simulated and measured responses of these filters is demonstrated.

Index Terms—Bandpass, coplanar waveguide (CPW), filter, high-pass, interdigital capacitor, millimeter wave, semilumped, W -band.

I. INTRODUCTION

MICROWAVE filters have been studied extensively using both lumped and distributed elements. However, the literature concerning planar millimeter-wave filters, especially at W -band frequencies, is scarce. There are few difficulties involved in filter design and fabrication at millimeter-wave frequencies and above. Most parasitic elements can usually be ignored at lower frequencies. However, their effects become significant at millimeter-wave frequencies. The parasitic elements and their effects cannot usually be considered as design parameters. Hence, they have to be accurately modeled and compensated for. To avoid dealing with the parasitic effects, use of structures with minimal parasitic features can be considered. Coplanar waveguide (CPW) line discontinuities are well characterized at microwave frequencies [1]–[3] and have been studied to some extent at higher frequencies up to 50 GHz [4]–[6]. However, modeling and characterization of such discontinuities at W -band frequencies is rather limited. Calibration accuracy at the W -band is one of the major difficulties in characterizing parasitic capacitances and inductances, which can be as small as a fraction of a femtofarad and a picohenry, respectively. Another problem is that the dimensions of typical lumped elements become comparable with the wavelength and require more complicated models to achieve reasonable accuracy. This paper provides accurate models for semilumped elements, which facilitate a systematic approach for filter design at millimeter waves.

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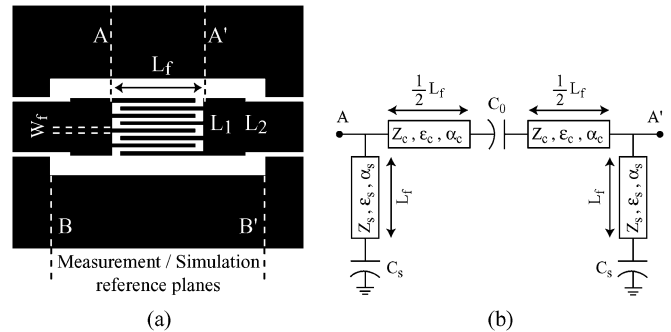


Fig. 1. Interdigital capacitor. (a) Layout. (b) Circuit model.

TABLE I
CPW LINE AND INTERDIGITAL CAPACITOR DIMENSIONS

W_{Line}	G_{Line}	W_{Finger}	G_{Finger}
$115\mu\text{m}$	$40\mu\text{m}$	$7.5\mu\text{m}$	$7.5\mu\text{m}$

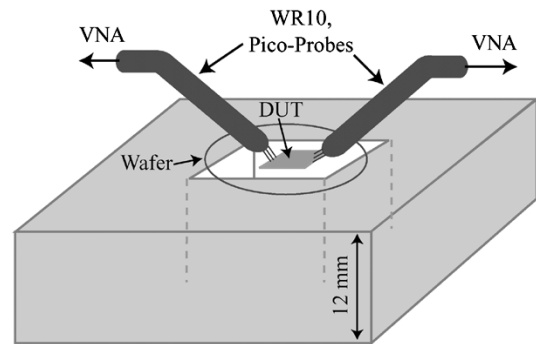


Fig. 2. Wafer holder with a cavity under the device-under-test (DUT).

TABLE II
MODEL PARAMETERS FOR INTERDIGITAL CAPACITOR

$Z_c(\Omega)$	$Z_s(k\Omega)$	ϵ_c	ϵ_s	$\alpha_c(\text{dB/cm})$	$\alpha_s(\text{dB/cm})$
96	5.4	2.5	3.4	2.5	3.7

Furthermore using a parasitic component of the proposed model as a design parameter, an out-of-band transmission null in a bandpass-filter (BPF) response is introduced.

In Section II, an accurate model for series interdigital capacitors in CPW lines is introduced and validated over a wide range of physical dimensions. In Section III, an existing model for shunt inductive stubs [7] is then modified to be valid at W -band frequencies. In Sections IV and V, design, fabrication, and measurements of a novel BPF and a miniaturized high-pass filter are presented, which make use of the semilumped elements.

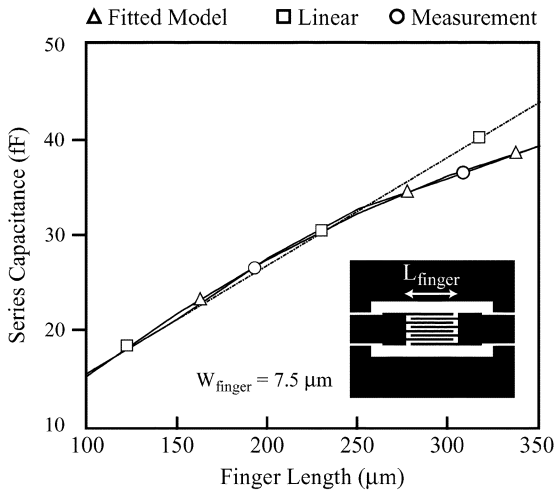
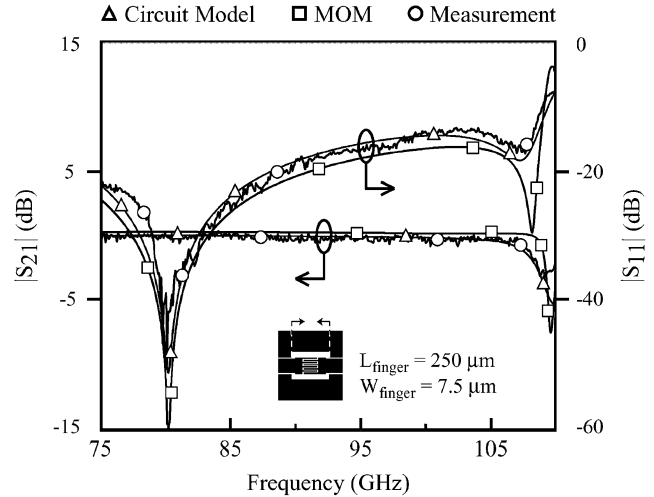
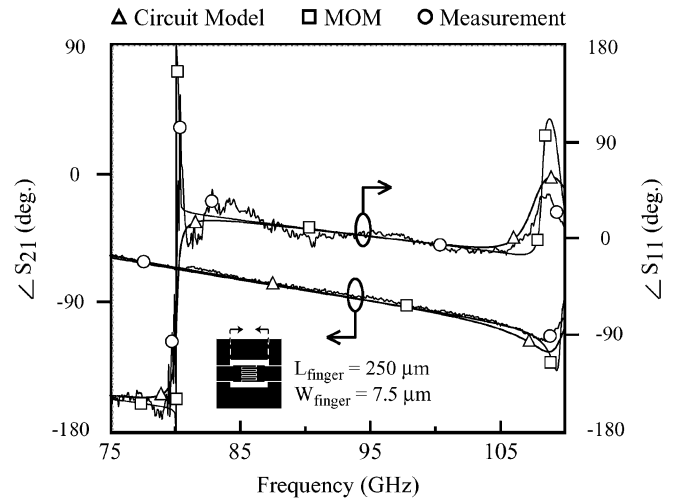


Fig. 3. Series capacitance of interdigital capacitors in CPW lines.

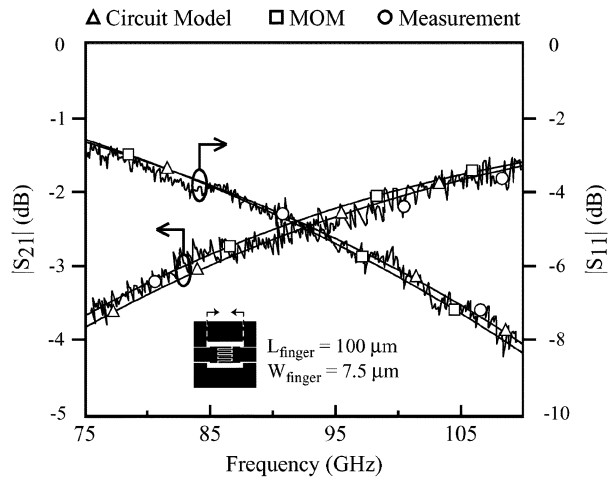


(a)

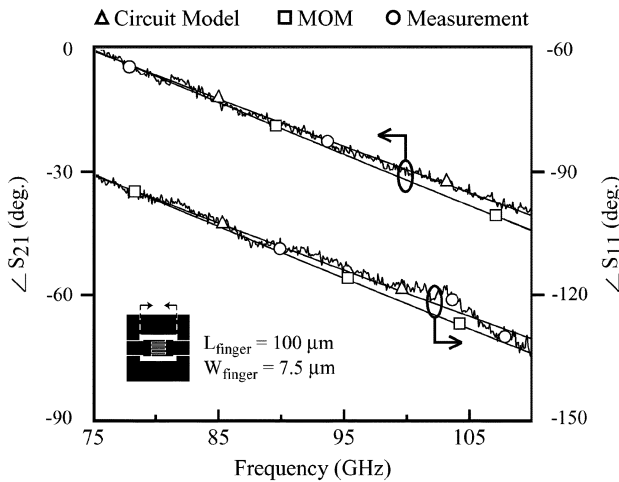


(b)

Fig. 5. Measured and simulated S -parameters of interdigital capacitor $L_f = 250 \mu\text{m}$. (a) Magnitude. (b) Phase.



(a)



(b)

Fig. 4. Measured and simulated S -parameters of interdigital capacitor $L_f = 100 \mu\text{m}$. (a) Magnitude. (b) Phase.

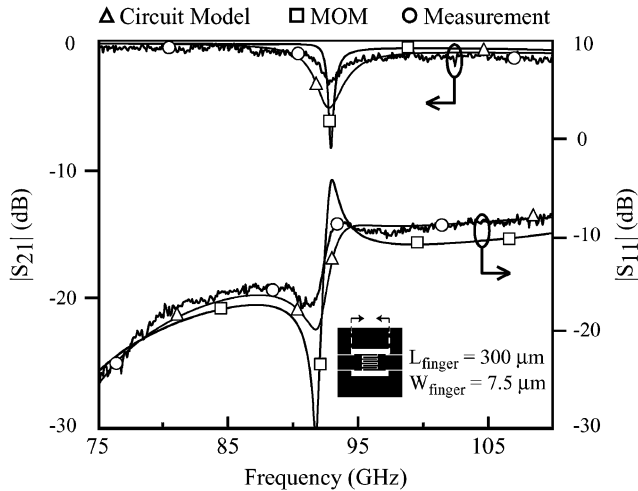
II. INTERDIGITAL CAPACITORS IN CPW LINES

Interdigital capacitors are used either far below their resonant frequencies [8]–[11] or as quarter-wavelength series open stubs [2], [6] to ensure the accuracy of simple existing models

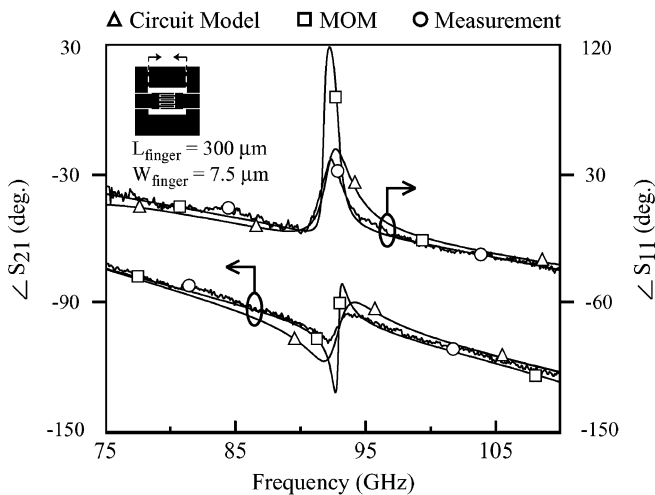
(lumped capacitor and open stub, respectively). For the quarter-wavelength open-stub case, complicated models for capturing the behavior of parasitic elements have been considered to increase the accuracy of the stub model [1], [12]. However, the large number of parameters introduced in these models limits their applicability. Despite the complexity, the accuracy of these models is still insufficient for applications in W -band frequencies.

Due to limitations in width of the center conductor of CPW lines (for preventing transversal mode radiation) and minimum achievable gap size (limited by the fabrication process), large capacitances can only be achieved by increasing capacitor finger length. Therefore, a complete model, which can accurately represent interdigital capacitor behavior over a wide range of finger lengths, and with a minimum number of parameters, is needed to facilitate design procedure.

Here, a new physics-based model is introduced and validated over the entire W -band frequency range for different capacitor finger length values. Fig. 1(a) and (b) shows the layout and circuit model of the interdigital capacitor, respectively. In order to extract the capacitor model, the effect of extra lines between A



(a)



(b)

Fig. 6. Measured and simulated S -parameters of interdigital capacitor $L_f = 300 \mu\text{m}$. (a) Magnitude. (b) Phase.

and B , as well as A' and B' , are deembedded. The discontinuity between lines L_1 and L_2 is negligible because the impedance change is only approximately 3%. For a fixed CPW line geometry, finger width (W_f), which is selected to be equal to gap width, determines the effective dielectric constants (ϵ_c, ϵ_s), the attenuation constants (α_c, α_s), and the characteristic impedances (Z_c, Z_s) of the series and shunt line segments of the circuit model. C_0 and C_s are functions of finger length (L_f) only.

The dimensions of a typical CPW line and interdigital capacitor finger length and gap width are given in Table I. The capacitor shown in Fig. 1(a) has been fabricated with different lengths ranging from 100 to 400 μm on a 10-mil-thick quartz wafer. The S -parameter measurements for these capacitors in W -band (75–110 GHz) were realized using a probe station (for on-wafer measurements), HP-8510C network analyzer, and HP-W85104A millimeter-wave test setup.

The probe station chuck, which holds the wafers, changes a CPW line to a conductor-backed CPW line and, thus, it affects the phase constant of the line [13] and the values of the parasitic elements. In order to eliminate these effects, a supporting structure with a cavity in the middle (shown in Fig. 2) is built and

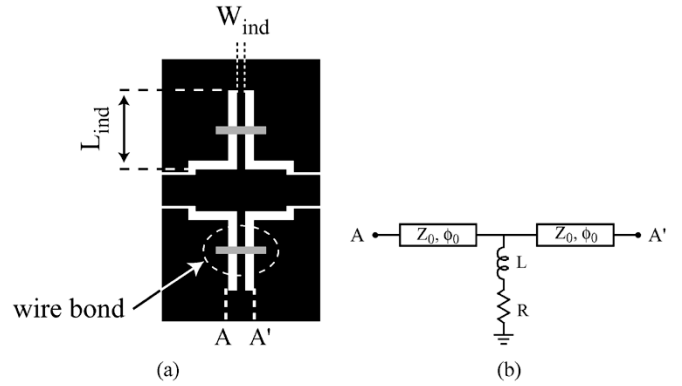


Fig. 7. Characterization of effective inductance and resistance for short stubs in CPW line. (a) Inductor layout. (b) Circuit model.

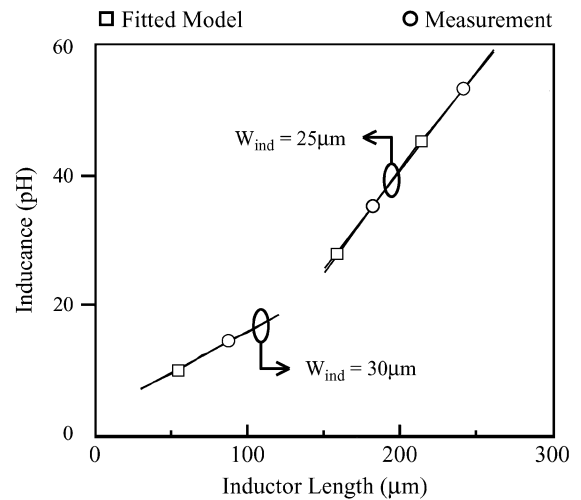


Fig. 8. Inductance of short-circuit stubs in CPW lines.

placed on top of the probe station's chuck. Method of moments (MOM) and circuit simulations are performed using the Agilent Advanced Design System (ADS). For the dimensions given in Table I, the effective dielectric constants, attenuation constants, and characteristic impedances of the circuit model are extracted by comparing the measured and simulated S -parameters (magnitude and phase) for 11 different cases (100–350 μm). These are provided in Table II. The values of C_0 and C_s are only dependent on normalized finger length ($l_f = L_f/L_{f0}$) and are given by

$$C_0 = C_{00} (-0.12l_f^2 + 1.19l_f - 0.09) \quad (1)$$

$$C_s = C_{s0}(0.6 + 0.4l_f) \quad (2)$$

where $C_{00} = 15.9 \text{ fF}$ and $C_{s0} = 0.1 \text{ fF}$ are values of C_0 and C_s at $L_{f0} = 100 \mu\text{m}$, respectively. The relation between the capacitance value and its physical length for short lengths is following a linear behavior, hence, these equations are expected to also be valid for length values smaller than 100 μm . The maximum error is less than 4% over the mentioned range.

Fig. 3 compares the series capacitance (C_0) value, given by (1), with its measured value. As shown, for finger-length values above 250 μm , capacitance does not increase linearly with finger length. To capture capacitance behavior for higher length

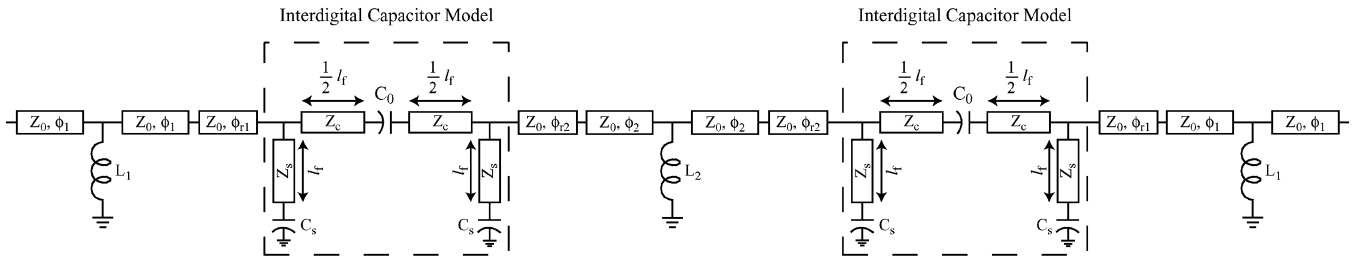


Fig. 9. New inductive coupled-resonator BPF circuit model.

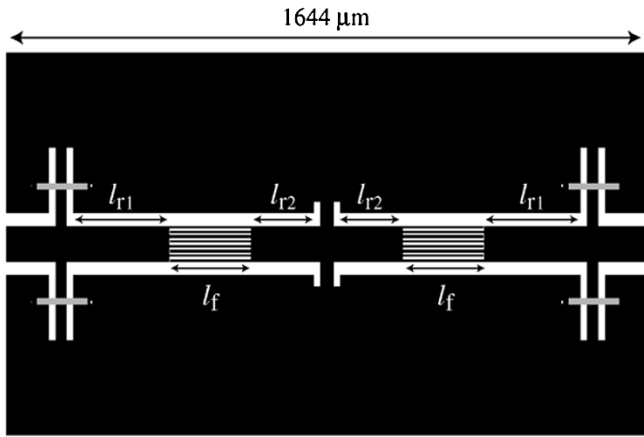


Fig. 10. New inductive coupled-resonator BPF layout.

values, a second-order term is provided in (1). Figs. 4–6 compare the measured S -parameters (magnitude and phase) with the MOM and circuit model simulation results for three different capacitor finger-length values of 100, 250, and 300 μm . The circuit model results show excellent agreement with the measurements in all situations. The circuit results show an even better agreement with the measurement than the MOM result. The reason is that MOM simulations in ADS for CPW structures are based on magnetic current modeling and, thus, cannot handle metallic losses.

III. SHUNT INDUCTORS IN CPW LINES

Here, short-circuit stubs, shown in Fig. 7(a), which are often used as inverters in filter designs [7], [14] are characterized at W -band for different stub length and width values. As the accuracy of the closed-form equation for inductance [7] is quite poor at W -band frequencies (errors often greater than 100%), inductance values have been extracted by comparison between simulation results for the circuit model shown in Fig. 7(b) and measured data for 20 different cases. The inductance values versus inductor lengths for two different inductor widths are shown in Fig. 8. As demonstrated, the inductance has a linear relation with the stub length within the selected range. This relationship can be described by

$$L_{\text{ind}} \text{ (pH)} = \begin{cases} L_{w1}(0.49l_1 + 0.5) & W_{\text{ind}} = 30 \mu\text{m} \\ L_{w2}(1.76l_2 - 0.75) & W_{\text{ind}} = 25 \mu\text{m} \end{cases} \quad (3)$$

where $L_{w1} = 7.7 \text{ pH}$ and $L_{w2} = 25.4 \text{ pH}$ are inductance values at $l_{10} = 30 \mu\text{m}$ and $l_{20} = 150 \mu\text{m}$, respectively, and $l_1 = L_{\text{ind}}/l_{10}$, $l_2 = L_{\text{ind}}/l_{20}$.

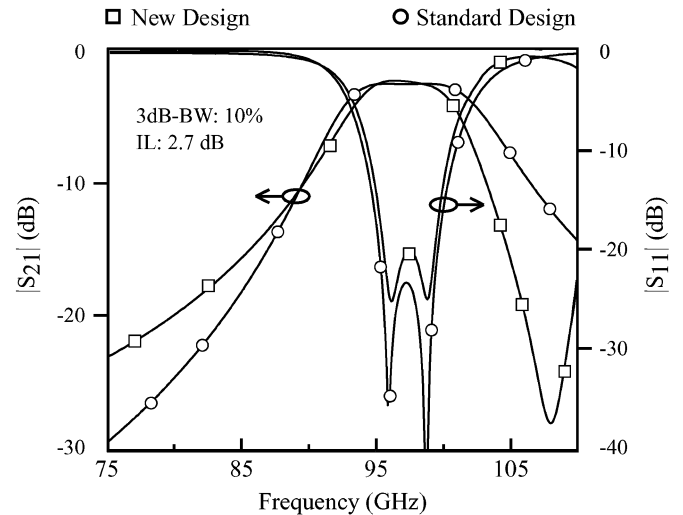


Fig. 11. Simulation results for the new inductive coupled-resonator BPF versus standard type of this filter.

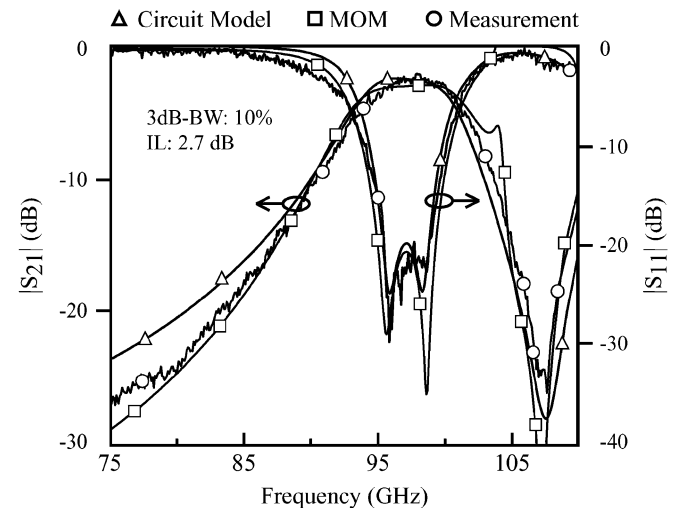


Fig. 12. Simulation and measurement results for the new inductive coupled-resonator BPF.

IV. BPF

Here, we demonstrate the usefulness of the accurate models of the semilumped elements introduced in the previous sections. A standard inductive coupled-resonator BPF [7] is modified by placing an interdigital capacitor in each resonators section, as shown in Figs. 9 and 10. The equivalent shunt loaded stubs of the interdigital capacitors [see Fig. 1(b)] provide a transmission

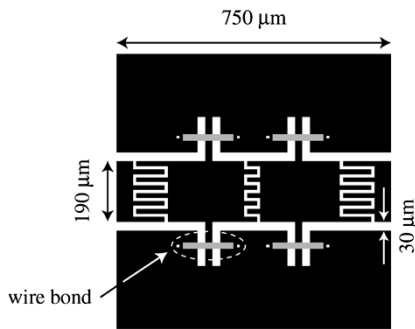


Fig. 13. Miniaturized high-pass filter layout.

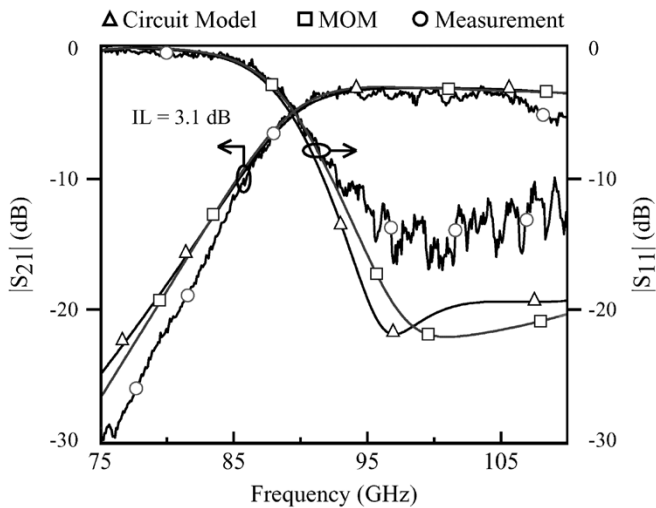


Fig. 14. Simulation and measurement results for the miniaturized high-pass filter.

null, and this is used to improve the out-of-band rejection response. The position of the null can be controlled simply by the length of the interdigital capacitor fingers. Fig. 11 compares simulation results for the new filter with a standard one. As shown, the 3-dB bandwidth of the new filter is slightly (10%) less than the standard filter, while the rejection at a desired frequency can be improved by 20 dB. Due to minor radiation loss from the interdigital capacitors, the quality factor (Q) of the resonators is slightly decreased in comparison with their values for the standard resonators. Consequently, the measured results of this filter shows 0.5 dB more insertion loss. This filter was fabricated on a 10-mil-thick quartz wafer using standard lithography and wet etching on 3- μm electroplated gold. Fig. 12 shows good agreement between the measured and simulated results of the circuit model. In Fig. 12, the nonphysical MOM response around 104 GHz occurs because of MOM simulator failure.

V. MINIATURIZED HIGH-PASS FILTER

The main goal of modeling semilumped elements is to use them as lumped elements in filter design. Lumped-element filters have very compact size, which can be beneficial in numerous applications. Using a ladder network of three series capacitors and two shunt inductors, which were modeled and described in Sections II and III, a miniaturized high-pass filter,

shown in Fig. 13, is designed. As can be seen, the total length of the filter is only 750 μm , which is less than $\lambda_g/3$ at the cutoff frequency of the filter. This filter was also fabricated on a quartz wafer, and its measured and simulated results are compared in Fig. 14. It can be seen that circuit model results have good agreement with both the MOM and measurement results. There are second-order effects such as interactions between inductors and capacitors that cannot be estimated by the circuit model, and these can be a source for degrading return loss. However, it should be noted the circuit model is still working as good as MOM.

VI. CONCLUSION

Simple and accurate models for two semilumped CPW elements have been presented and validated at W -band frequencies for different dimensions. The simulation results of the extracted models show good agreement with the measured results. The models greatly simplify the process of filter design at W -band and higher frequencies. The semilumped elements have been used for design of two millimeter-wave filters (a BPF and high-pass filter), and have helped to improve filter response in the first case and miniaturization in the second case.

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