

PINAKI MAZUMDER

Teaching Accomplishments

I Received a Letter of Commendation for Teaching from the Dean of College of Engineering.

Courses Taught and Developed

1. Winter 2003: EECS 270: Logic Design
Evaluation: 4.02/5.0 (first item) and 3.77/5.0 (second item)
2. Fall 2002: EECS 579: Digital Testing
Evaluation: 3.88/5.0 (first item) and 3.90/5.0 (second item)
3. Winter 2002: EECS 270: Digital Logic Design
Evaluation: 4.25/5.0 (first item) and 4.33/5.0 (second item)
4. Winter 2001: EECS 270: Logic Design
Evaluation: 4.02/5.0 (first item) and 4.32/5.0 (second item)
5. Fall 2000: EECS 270: Digital Logic Design
Evaluation 3.74/5.0 (first item) and 4.17/5.0 (second item)
6. Fall 1999: EECS 427: VLSI Design
Evaluation: 4.05/5.0 (first item) and 3.50/5.0 (second item)
7. Fall 1998: EECS 270: Logic Design
Evaluation: 4.00/5.0 (first item) and 3.99/5.0 (second item)
8. Winter 1999: EECS 579: Digital Testing
Evaluation: 3.42/5.0 (first item) 3.20/5.0 (second item)
9. Fall 1998: EECS 270: Digital Testing
Evaluation: 4.00/5.0 (first item) and 3.99/5.0 (second item)
10. Winter 1998: Digital Logic
Evaluation: 4.00 (first item) and 3.99 (second item)
11. Fall 1997: Taught EECS 427: VLSI Design
Evaluation: 4.71 (first item) and 4.58 (second item)
12. Winter 1996: Taught EECS 527: Computer-Aided Design for VLSI Systems
Evaluation: 4.50 (first item) and 4.10 (second item)
13. Fall 1995: Taught EECS 427: VLSI Design
Evaluation: 4.55 (first item) and 3.94 (second item)
14. Winter 1995: Taught EECS 527: Computer-Aided Design for VLSI Systems
Evaluation: 4.25 (first item) and 4.08 (second item)
15. Fall 1994: Taught EECS 427: VLSI Design
Evaluation: 4.81 (first item) and 4.12 (second item)
16. Fall 1993: Taught EECS 427: VLSI Design
Evaluation: 4.32 (first item) and 3.83 (second item)
17. Spring 1992: Taught EECS 270: Digital Logic Design
Evaluation: 4.6 (first item) and 4.43 (second item)
18. Winter 1992: Taught EECS 527: Computer-Aided Design for VLSI Systems
Evaluation: 4.00 (first item) and 4.25 (second item)
19. Fall 1991: Taught EECS 427: VLSI Design
Evaluation: 4.13 (first item) and 3.88 (second item)
20. Spring 1991: Taught EECS 270: Digital Logic Design
Evaluation: 4.54 (first item) and 4.71 (second item)
21. Winter 1991: Taught EECS 570: Advanced Computer Architecture
Evaluation: 4.20 (first item) and 3.89 (second item)

Legend: 5.0 – Excellent, 4.0 – Very Good, 3.0 – Good, 2.0 – Fair, 1.0 – Poor.