

**Problem 1.**

Using Boolean Algebra, show that (Proofs by Truth Table are not acceptable):

- (a)  $(X + Y)' = X' \bar{Y} = \bar{X} + Y'$
- (b)  $(X + Y) + Z = X + (Y + Z) = X + Y + Z$
- (c)  $AB + BC + CA = (A+B)(B+C)(C+A)$
- (d)  $X\bar{Y} + XYZ + \bar{X}Z = (\bar{X}\bar{Z} + YZ)'$

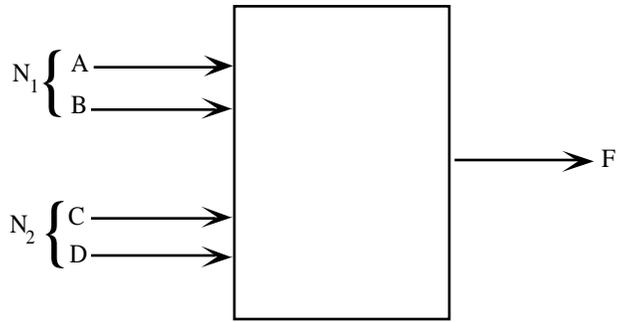
**Problem 2.**

Simplify the following expressions as possible by using Boolean algebra:

- (a)  $XY + \bar{X}Y\bar{Z} + YZ$
- (b)  $X\bar{Y} + Z + (\bar{X} + Y)\bar{Z}$
- (c)  $\bar{X}Y + YZ + XY + \bar{Y}\bar{Z}$
- (d)  $\bar{X}\bar{Y} + YZ + XZ + XY$

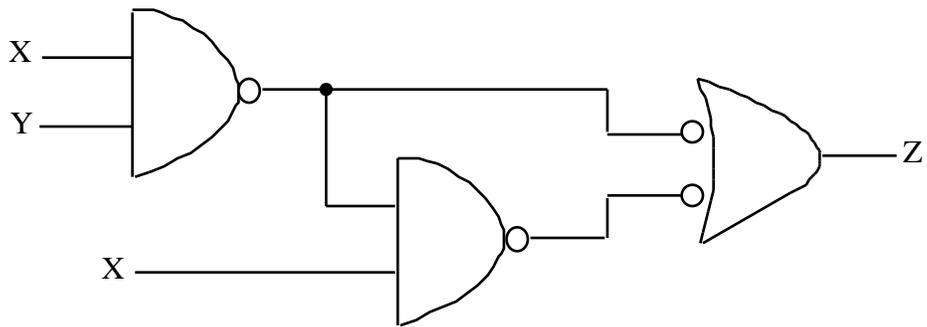
**Problem 3.**

- (a) A combinational network has 4 inputs (A,B,C,D) and three outputs (X,Y,Z). XYZ represents a binary number whose value equals the number of 1's at the input. For example, if ABCD = 1011, XYZ = 011.
  - a. Find the minterm expansions for X, Y and Z.
  - b. Find the maxterm expansions for Y and Z.
- (b) A switching network has 4 inputs as shown below. A and B represent the first and second bits of a binary number  $N_1$ . C and D represent the first and second bits of a binary number  $N_2$ . The output of the network is to be 1 only if the product  $N_1 \times N_2$  is greater than two. A and C are the most significant bits of  $N_1$  and  $N_2$ , respectively.
  - a. Find the minterm expansion for F.
  - b. Find the maxterm expansion for F.

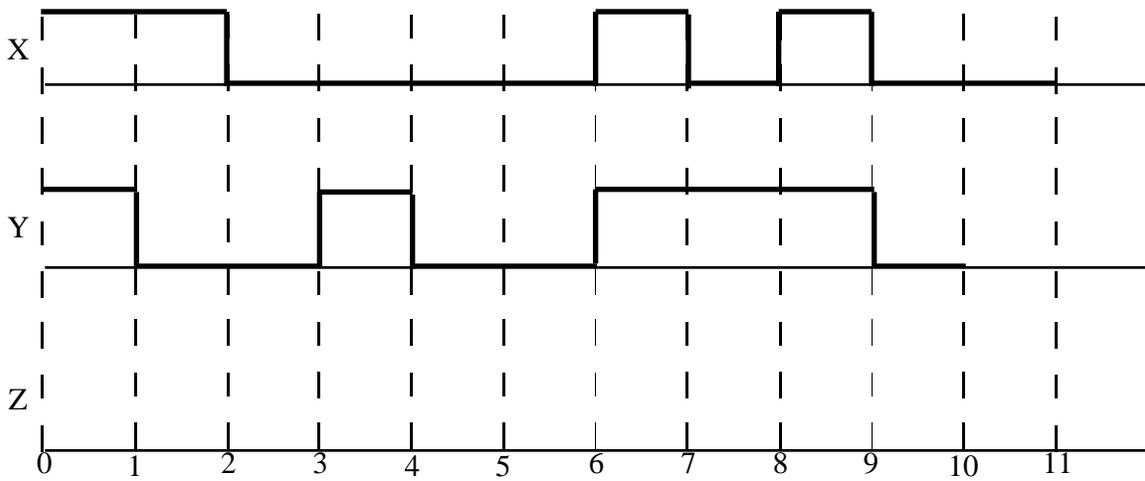


**Problem 4.**

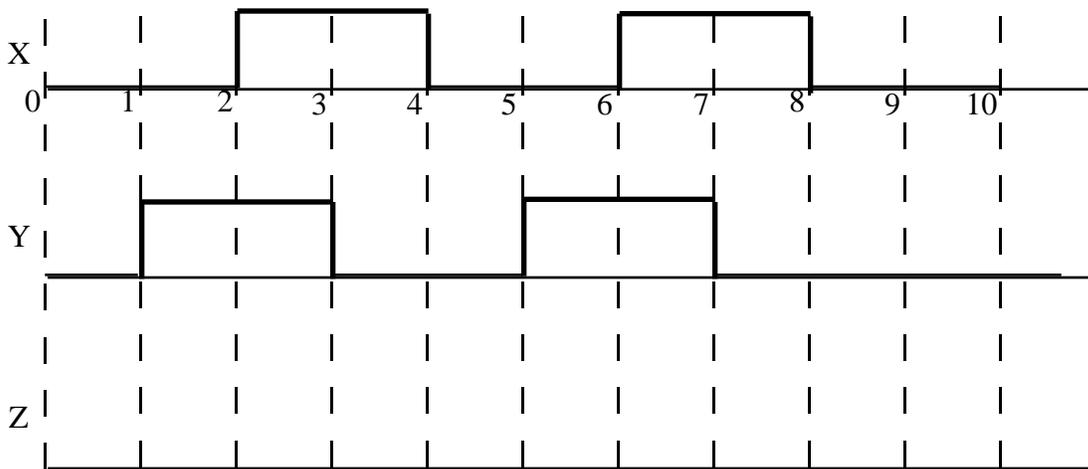
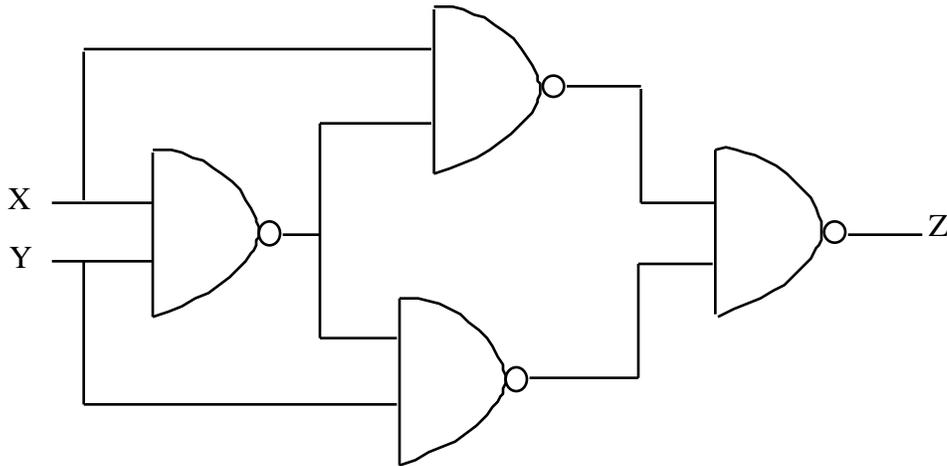
(a) Given the following network, write an expression for Z and simplify.



Assume that each gate has 1 unit of delay. (Delay time = 1 on timing diagram.)  
 Draw the output waveform (Z) for the given input values of X & Y.



(b) Obtain an SOP (sum of products) expression for Z. Draw the output waveform (Z) for the given values of X and Y. Assume each gate has zero delay.



**Problem 5.** Textbook problem 4.46

**Problem 6.** Textbook problem 4.49

**Problem 7.** Textbook problem 4.55

**ALL HOMEWORK MUST BE TURNED IN DURING LECTURE TIME, OTHERWISE IT WILL NOT BE GRADED.**