DEVELOPMENT OF A SUPERCONDUCTING INTEGRATED REceiver FOR APPLICATION IN IMAGING ARRAYS

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Introduction

An imaging array for the sub-millimeter wavelength region based on a number of light weight and low power consuming devices such as the recently developed superconducting integrated receiver [1], looks very attractive, especially for space applications. Both reproducibility and reliability of the integrated receiver chips are very important to create an imaging array. We have demonstrated recently a noise temperature of 400 K DSB at 450 GHz using an on-chip Flux Flow Oscillator (FFO) as a local oscillator (LO). This was only 30% higher than the best performance obtained by using an external LO [2]. The main reason for the difference between the external and internal pump is lack of the LO power coupled from FFO to the SIS junction.

The experiment with frequency locking of the FFO at about 450 GHz to an external synthesized source of 10 GHz has been performed successfully. An averaged linewidth of about 200 kHz has been recorded [3]. The recent status of an integrated receiver study as well
as a number of specific problems associated with the development of an imaging array are reported.

**Experimental Details**

The all-Nb superconducting integrated receiver on a single crystalline quartz chip of size 4 mm × 4 mm × 0.2 mm contains: a planar double-dipole antenna, SIS mixer, FFO as a local oscillator and all necessary coupling circuitry. The photo of the chip receiver mounted in the mixer block by Al bonding (Ø20 μm) is presented in Fig. 1.

The transfer of the production process from IREE (Moscow, Russia) to SRON/FDL (Groningen, the Netherlands) has been realized successfully in a short time, which probably means that the integrated receiver chips are reasonably easy to produce world-wide. All the circuits have been redesigned for use of SiO₂ insulation at SRON/FDL instead of SiO used at IREE. A yield of good devices as high as 80% has been achieved in a single batch (12 useful devices from 15 ones available). The data of production yield are presented in Table 1.

The optimization of the coupling circuitry between the SIS mixer and the FFO has resulted in considerable improvement of the pumping level of the SIS mixer [4]. The normalized RF voltage $\alpha = eV_{RF}/hf$ as high as $\alpha = 2$ has been achieved during preliminary dc-tests in dipstick. In the cryostat the antenna is matched better to the outcoming beam so part of LO power emitted those results in somewhat lower pump level.

The use of μ-metal shield around the housing of the integrated receiver in the cryostat has resulted in the improvement of stability of the FFO in the noisy experimental environment. The recent improvements in the optics (anti-reflection coating on all quartz lenses of the mixer, thin mylar window of the cryostat) have resulted in significant decrease of the receiver DSB noise temperature lower than 150 K. The break-down of the noise for the experimental receiver is presented in Table 2.

It has been found that no significant difference occurs for the receiver driven with external or internal LO for both the pump level and the noise temperature of the receiver. The experimental data for the mixer as well as its hot/cold response at about 500 GHz are presented in Fig. 2. The plot of the receiver DSB noise temperature with respect to the LO
frequency is presented in Fig. 3.

The experimental antenna beam pattern of the integrated receiver is presented in Fig. 4. The difference in the width of H- and E-pattern is most probably caused by the double-dipole antenna shape which is designed to be used without back-reflector that means longer arms and higher directivity in the vertical plane.

The reported results of the integrated receiver look rather encouraging that allow us to start development of optical concept for integrated image array receiver.

Optical Configuration for an Image Array Receiver

There are several possible optical configurations that could be used in an imaging array. Two different approaches of practical interest to an array are: A) a single dielectric lens antenna (either an elliptical or hyper hemispherical lens, Figure 5a) with an array of antennas (receivers) positioned in the focus of the lens, and B) an array of lenses each with its own receiver positioned in the focus of the lens (fly’s eye, Figure 5b). With the single lens concept an additional intervening optical system (mirrors and/or lenses) between the telescope and the dielectric lens antenna is needed to re-image the beams onto the sky, as for the fly’s eye concept that depends on the dielectric lens type chosen.

- Figure 5. Two different array concepts for 9 pixel image receiver: a) A large single lens with
an array of receivers in its center, and b) an array of lenses, each only having a single element in its center.

Comparison of different optical schemes needs to be based on the performance, such as aperture efficiency, spillover, undersampling (packing density), sidelobe level and Gaussian beam coupling, of each system. Also practical considerations, such as the size of the whole array, the size of the intervening optics and whether there is a need for them, yield in the SIS-junction process, testability and selection of each receiver in the array, play an important role in selection of the best concept for the imaging array.

Both the single lens and the fly’s eye concept offer considerable advantages (⊕) as well as disadvantages (⊖), which are more or less reciprocal of one another:

**Single lens**

⊕ everything on a single chip, superconducting lines for DC and RF connections could be used (including phase-locking signals),

⊖ small size,

⊖ ease of mounting,

⊖ the image quality of the off-axis beams distorted due to aberrations,

⊖ crosstalk between individual elements,

- mutual coupling (RF),
- intermediate frequency coupling (IF),
- magnetic coupling (DC),
- reflections from the dielectric-air interface,

⊖ a large front lens needed,

⊖ yield in the SIS manufacturing process might not be high enough to give 9 (10) similar junctions on a single chip (plus FFOs),

⊖ broken pixel is non-replaceable since the receivers are manufactured on a single chip.
"Fly's eye"

- all antennas are center elements ⇒ no aberrations,
- possible to match directly to the telescope without any intervening optical system,
- reduced crosstalk between individual elements,
- each of the receivers can separately be tested and replaced, if needed,
- size of the receiver chip much smaller than the lens size ⇒ room for connections,
- large array size ⇒ large intervening optics if they are needed (sideband filtering, calibration etc.),
- large number of individual lenses and receives ⇒ mounting laborious.

The problems associated in the single lens concept with the integrated receiver, such as crosstalk, packing density and yield in the SIS junctions process are obstacles that could be overcome with thorough study and testing with time. On the other hand, the "fly's eye" concept already offers solution to these problems, and in addition it offers high beam quality for all beams, since there are no aberrations. For the reasons presented we are in favour of the "fly's eye" configuration, but we have not yet completely ruled out the single lens concept either.

**Conclusion**

The recent results of the integrated receiver test: $T_{RX} < 200$ K, reasonably low antenna sidelobes (-18...-20 dB), and quite high production yield (80%), allow us to start the development of a concept of the integrated image array for radio astronomical application.

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References:


Figures Capture:

Fig. 1 Photo of the chip receiver mounted on the hyper hemispherical lens by bonding.

Fig. 2 Experimental data for the mixer pumped by FFO: autonomous and pumped IV-curves; hot/cold response at about 500 GHz.

Fig. 3 Integrated receiver DSB noise temperature with respect to the FFO frequency.

Fig. 4 Experimental antenna beam pattern of the integrated receiver.
Integrated Receiver with FFO as Local Oscillator

Fig. 1

Fig. 2
Fig. 3

Receiver DSB Noise Temperature of Integrated Receiver H5.2#14

Freq. (GHz)  14 February 1995

Fig. 4

Antenna Beam Pattern of Integrated Receiver H5.2#14

Rotation angle (Degree)  22 Nov. 1995
### Table 1
Results of dc tests of Integrated Receiver Chips H5 (batch #2)

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<td>32/</td>
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<td>1000</td>
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$SIS V_g$ ($\mu$V) Before modification of the measuring setup $V_g$ was measured by oscilloscope.

$SIS CL$ 1st min/2nd min (mA) Values of the SIS Control Line Current where 1st /and 2nd minimum of SIS critical current occur.

$SIS area$ ($\mu$m$^2$) Calculated from averaged $R_S S$ value.

FFO $\Delta I_L$ (mA) FFO “gap” current.

FFO $I_L$ (mA) FFO “return” current.

FFO $V_g$ ($\mu$V) Measured at 10 mA.

FVO $V_g$ ($\mu$V) FFO voltage where maximum pumping of SIS takes place.

$\Delta I$ SIS at max P. Maximum changing of the SIS current ($\mu$A) at 2 mV by FFO pumping.

$\Sigma$ Summary Rating (possibility to use for some hf measurements: IntRec, Receiver with External LO, FTS)

### Table 2
Noise Figure Breakdown for Quasi-optical Integrated Receiver H5-2#7

<table>
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<tr>
<th>$T_R$ DSB (K)</th>
<th>$T_R$ DSB corrected for beamsplitter 10 $\mu$m (K)</th>
<th>$T_R$ DSB corrected for 10% sidelobes (K)</th>
<th>Mixer noise $@$ input (K)</th>
<th>IF noise $@$ input (K)</th>
<th>Optics efficiency (dB)</th>
<th>Antenna coupling (dB)</th>
<th>Mixer gain DSB (dB)</th>
<th>IF coupling $R_{260}=1900$ (dB)</th>
<th>$T_H$ (K)</th>
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<td>-</td>
<td>-</td>
<td>-</td>
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<td>9±3</td>
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<td>Theoretical</td>
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<td>54</td>
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<td>-1.8</td>
<td>12</td>
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</table>

1 Noise of the optics and side lobes included

2 Part of the mixer gain

3 Quantum noise not included (26 K @ 500 GHz)