

WISE 2000 - International Workshop on Spectroscopic Ellipsometry
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Real-time, In-line and Integrated Metrology of SiGe/Si Structures

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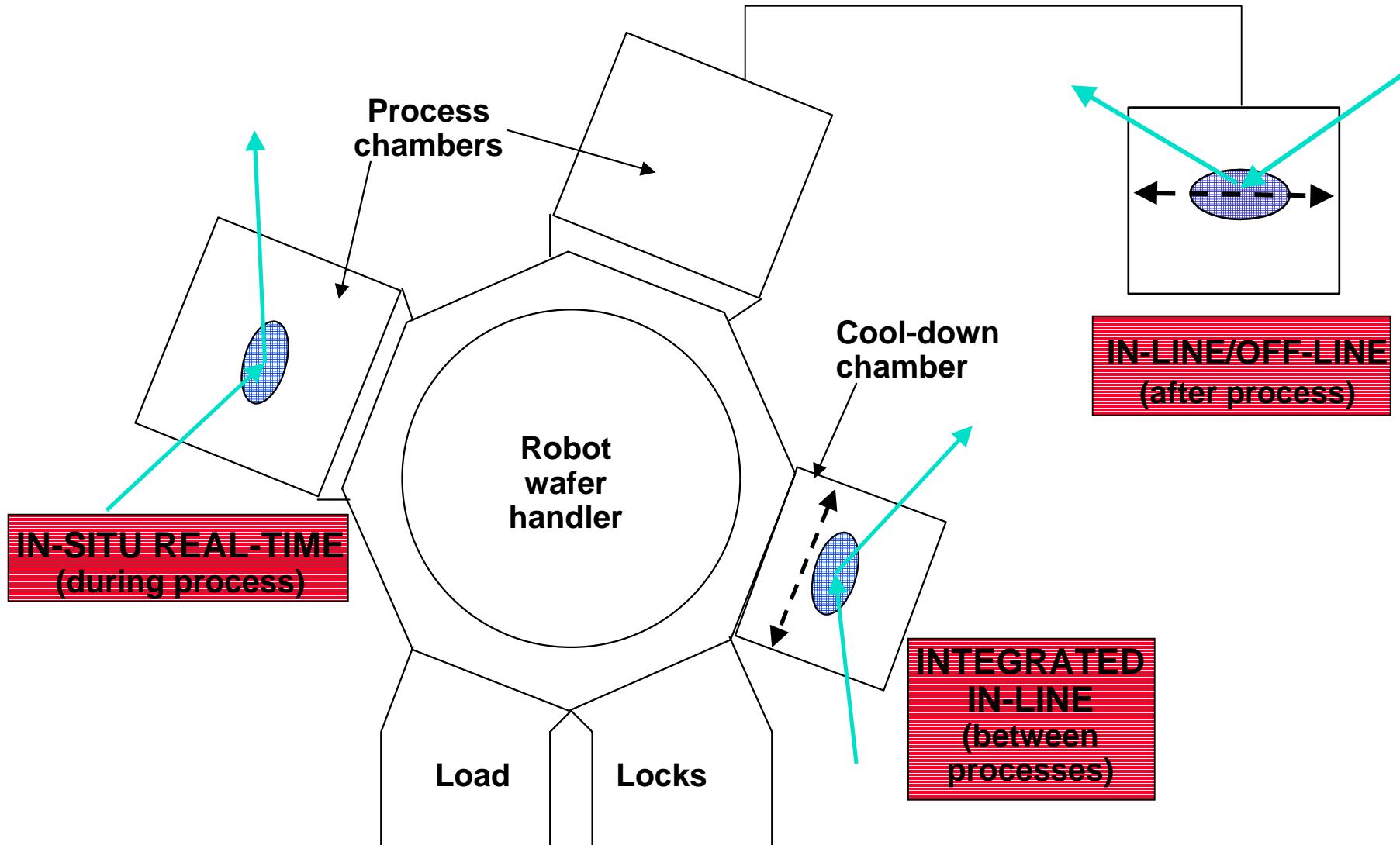
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Outline of Presentation

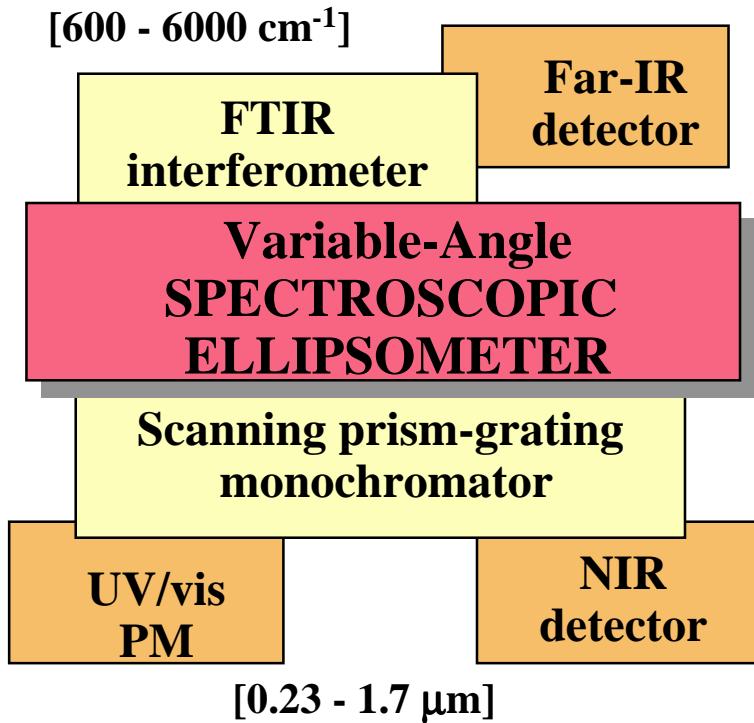
- Introduction
- Optical and epitaxy facilities
- Ex-situ characterisation of strained SiGe structures
 - *in-line SE for SiGe process development*
- Real-time in-situ monitoring/control of SiGe
 - *data analysis methods*
- Development of integrated metrology
 - *advantages and future work*

INTEGRATED METROLOGY USING SPECTROSCOPIC ELLIPSOMETRY



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SPECTROSCOPIC ELLIPSOMETRY



In-line: Si Cleanroom

Robot wafer-handling
Pattern recognition
Scribe line measurements
Multilayer mapping
High throughput

AUTOMATIC SE Multi-Layer Monitor

In-situ: SiGe reactor

REAL-TIME SE + LASER LIGHT SCATTERING

[0.25 - 0.82 μm
512 wavelengths
5 spectra per sec]

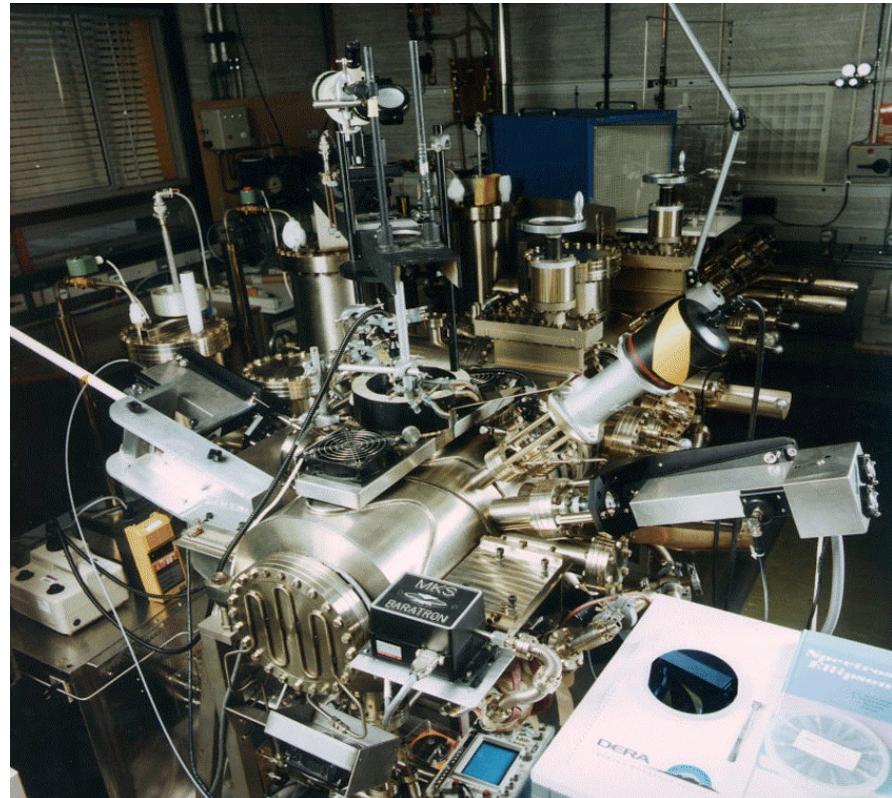
AMAT Epi Centura Cluster Tool

Combination of MLM and RTSE Cool-down chamber measurements

INTEGRATED METROLOGY SE

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Advanced Low-Temperature Epitaxy at DERA



Applied Materials Epi Centura ®

- single wafer cluster tool wafer sizes:
100mm, 150mm, 200mm
- standard reduced pressure processes
- new low pressure process
- n- and p-type Si and SiGe multi-layers
- selective and non-selective epitaxy
- strain-relaxed SiGe virtual substrates
- integrated metrology under development

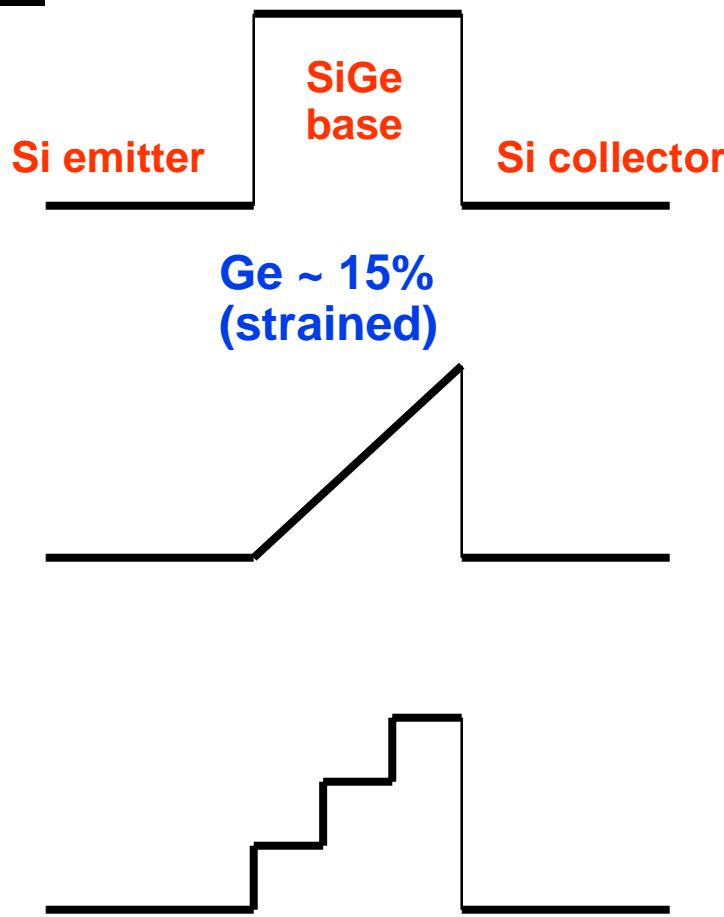
Custom low-pressure cold-wall reactor

- UHV background pressure
- 100mm, single wafer
- *in-situ* diagnostics for process control
- n- and p-type Si and SiGe multi-layers
- SiGe virtual substrates
- high mobility 2D electron gases in strained-Si layers
- SiGe/Si multi-quantum well structures

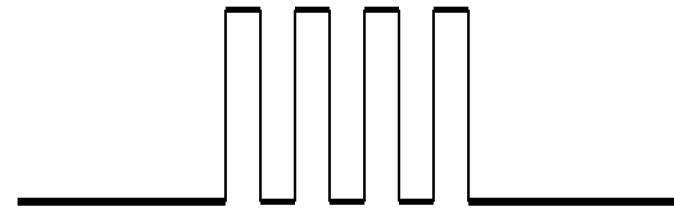
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Representative Device Structures

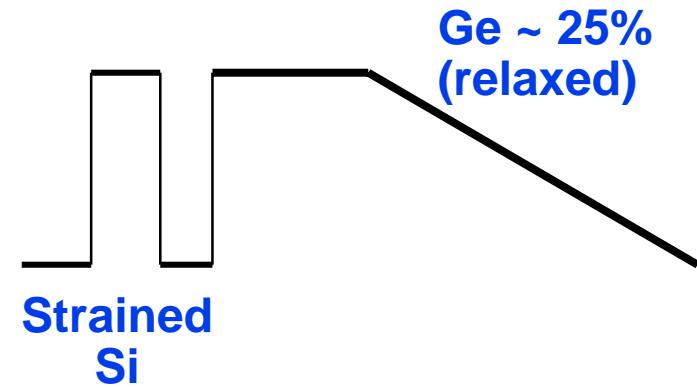
HBT



QWIP

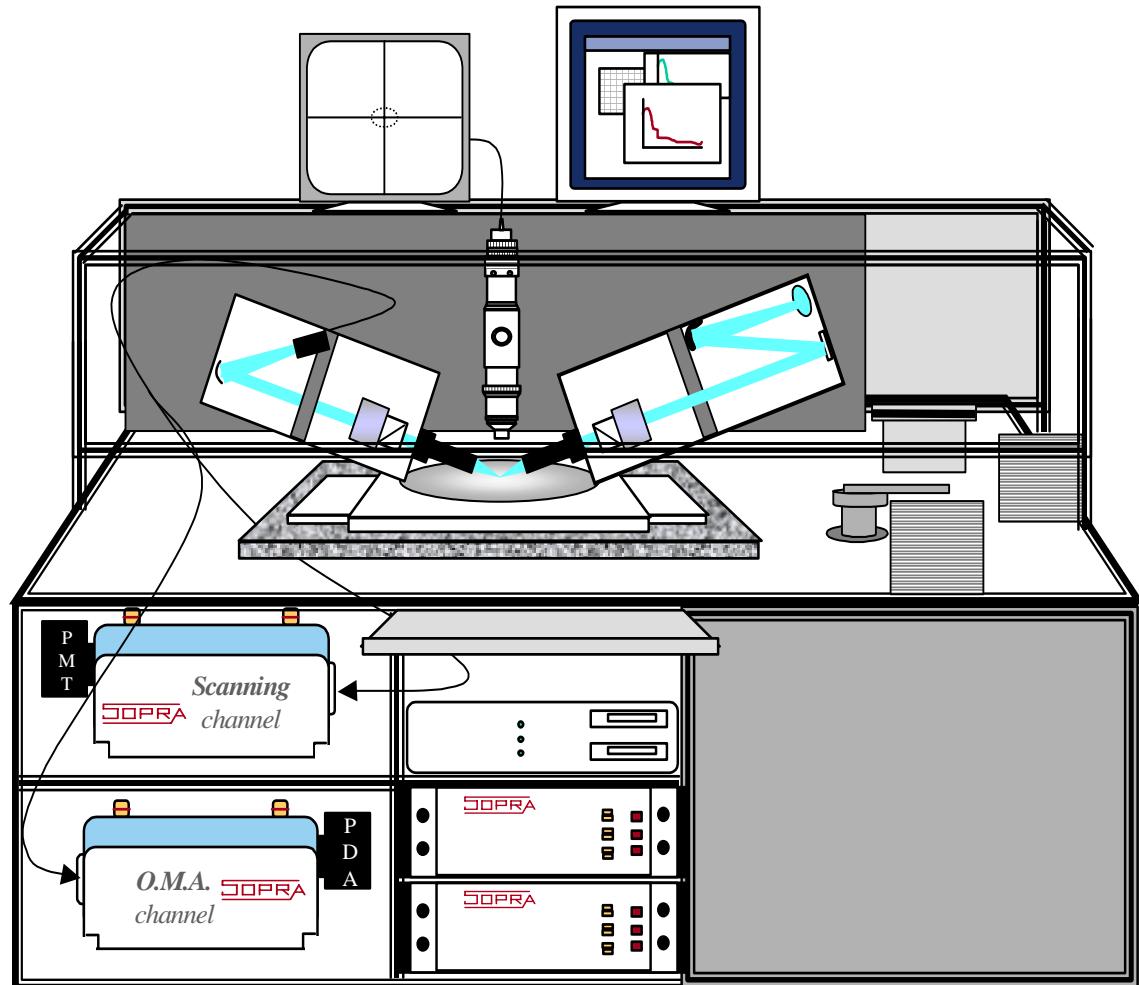


HCMOS

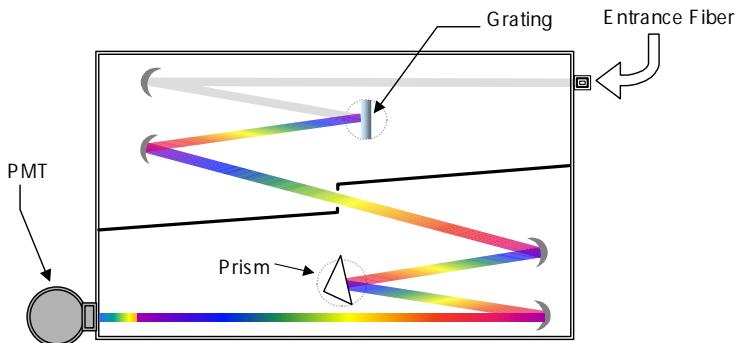


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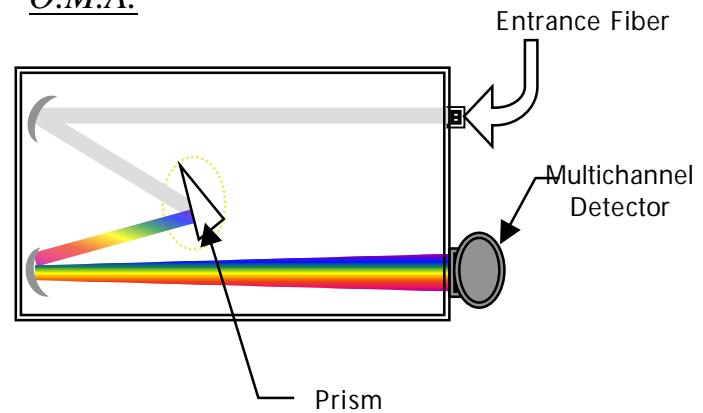
SOPRA Multi-Layer Monitor



Scanning

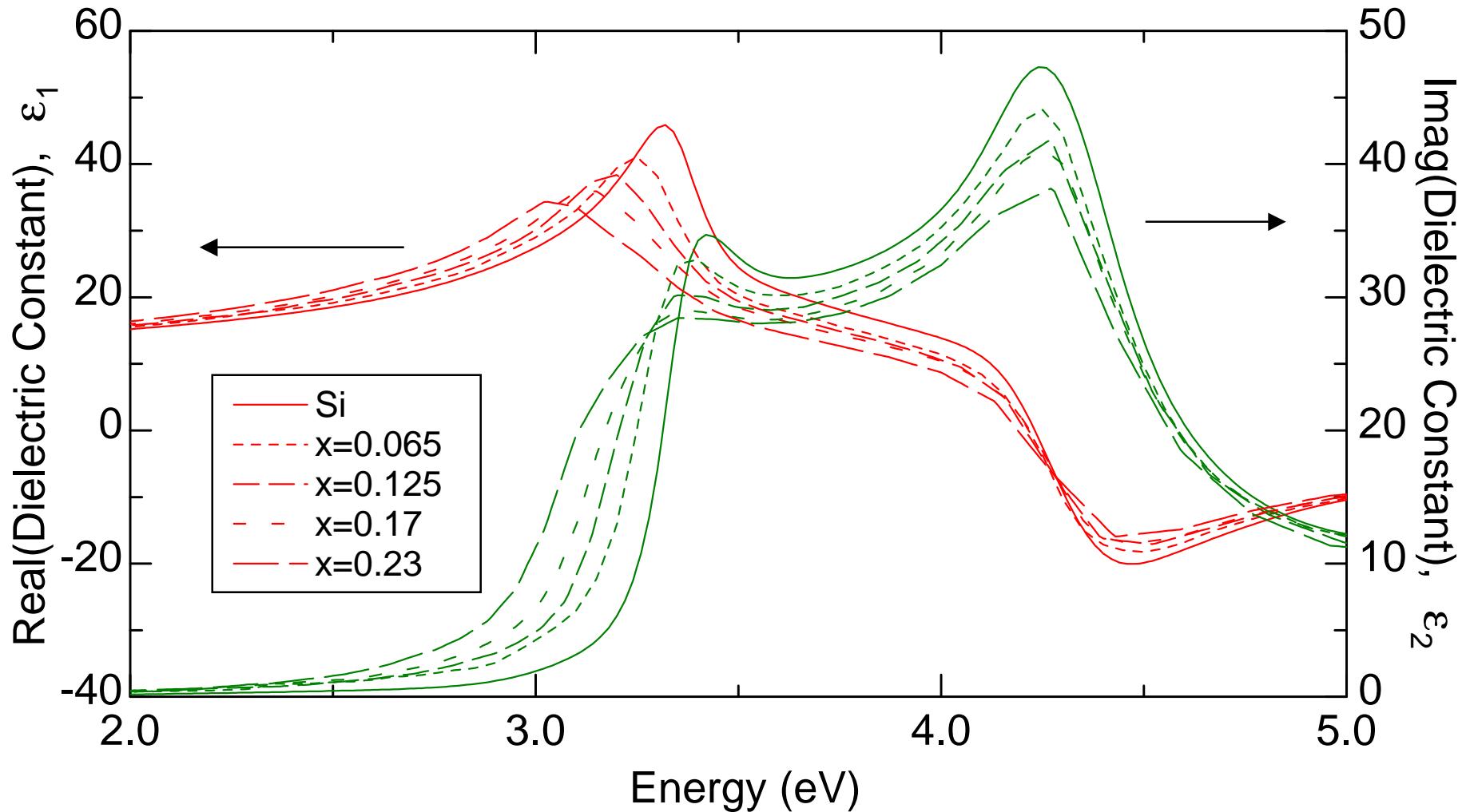


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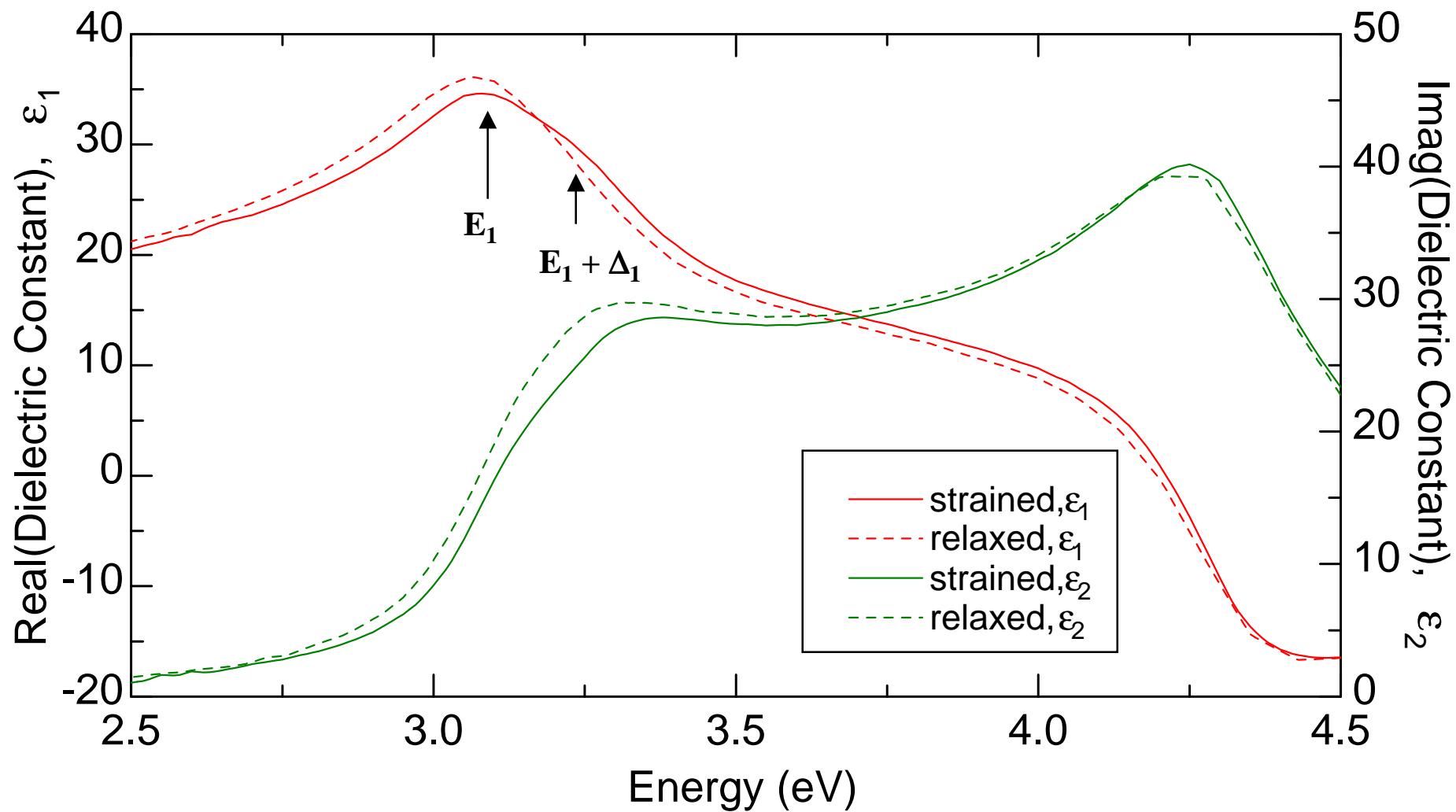


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Strained Si(1-x)Ge(x)

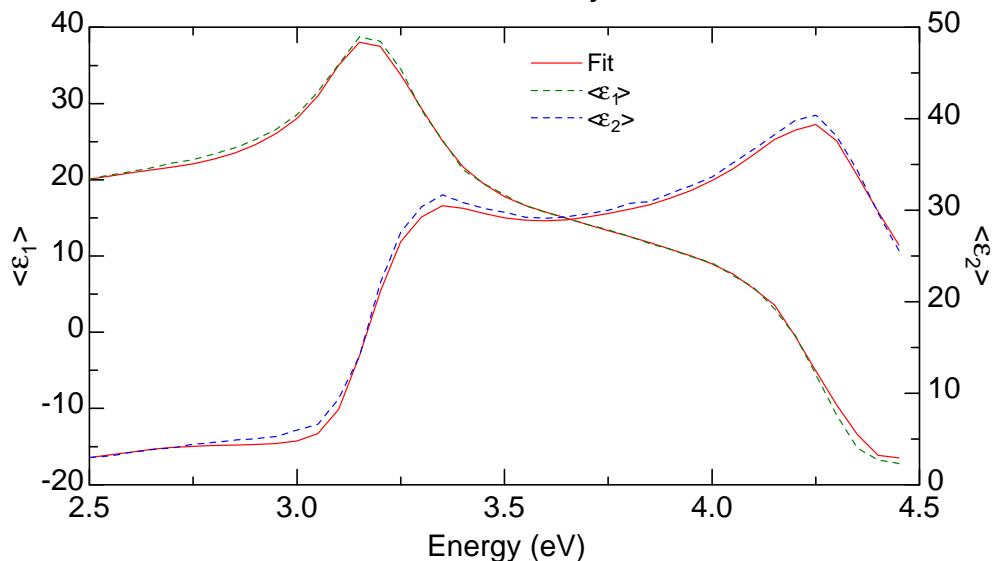


Strained and Relaxed Si(0.8)Ge(0.2)

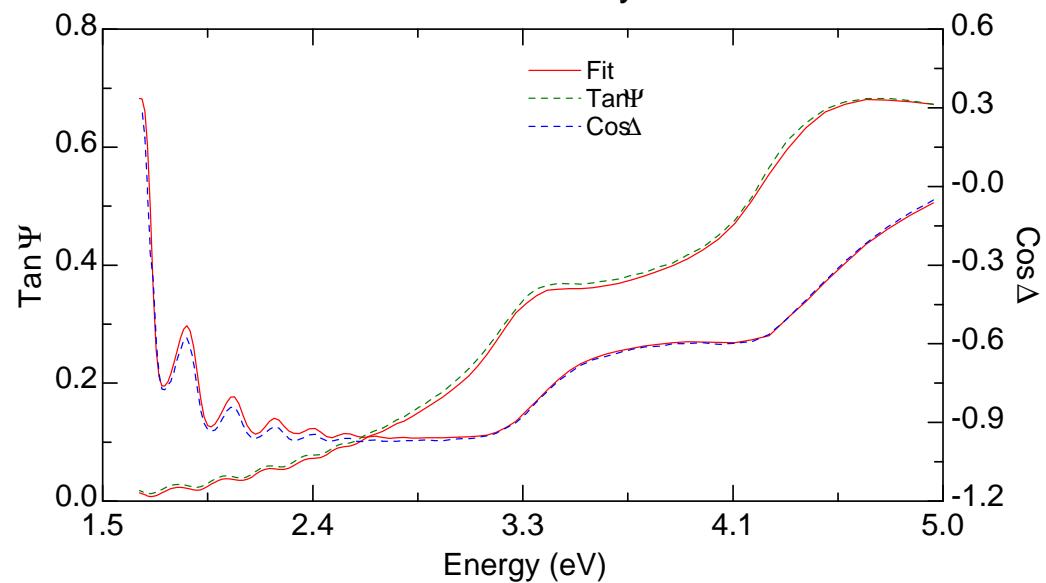


SE Characterisation of Single SiGe Epitaxial Layers

Thin SiGe Layer



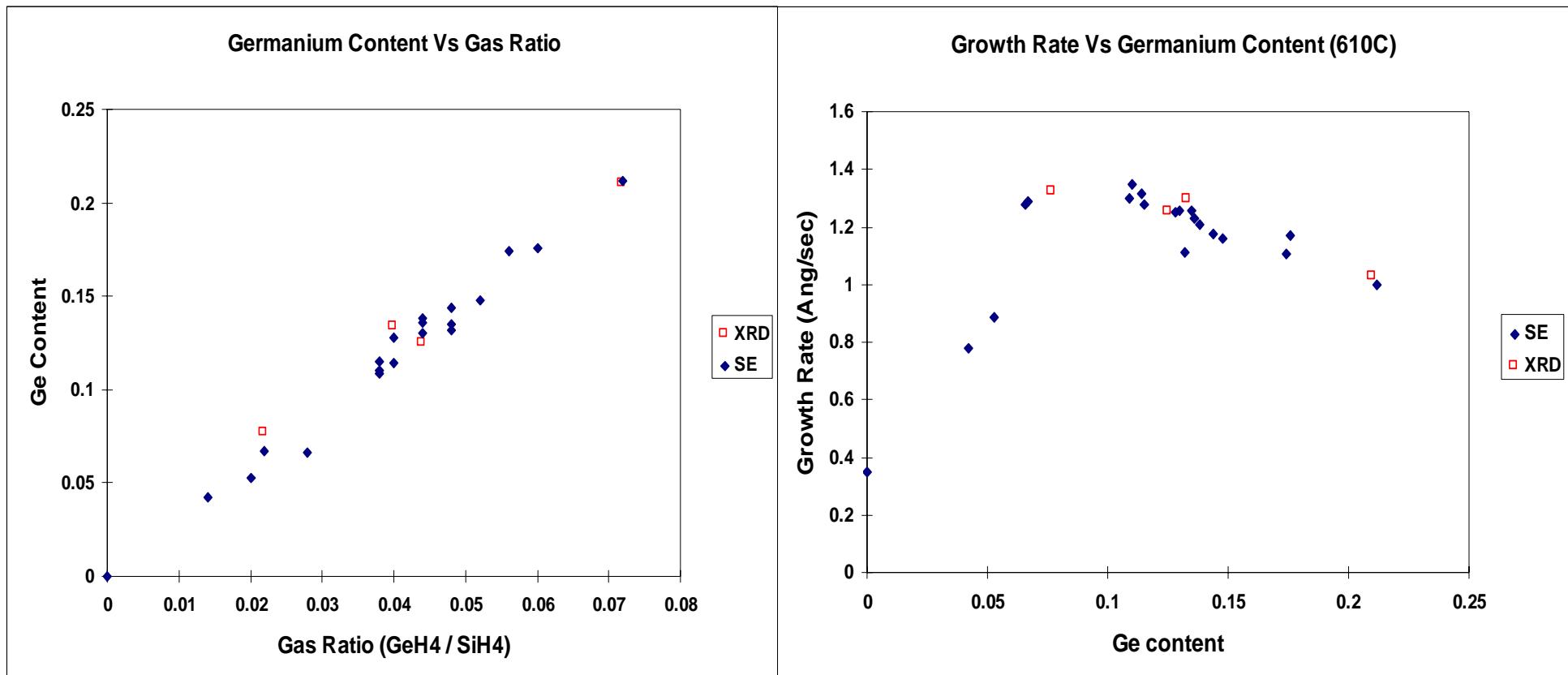
Thick SiGe Layer



| | | |
|---|----------------|----------|
| 2 | sio2 | 3.8448 Å |
| 1 | sigewv x=0.140 | 359.57 Å |
| 0 | si | 1 mm |

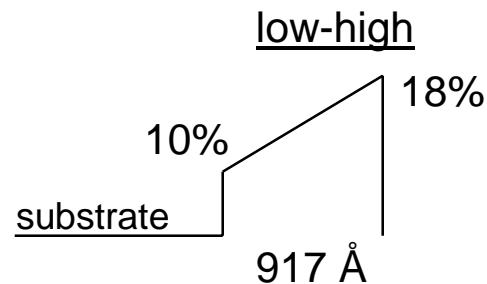
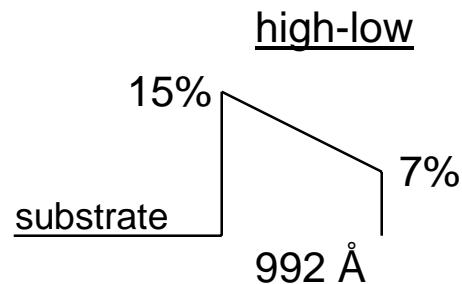
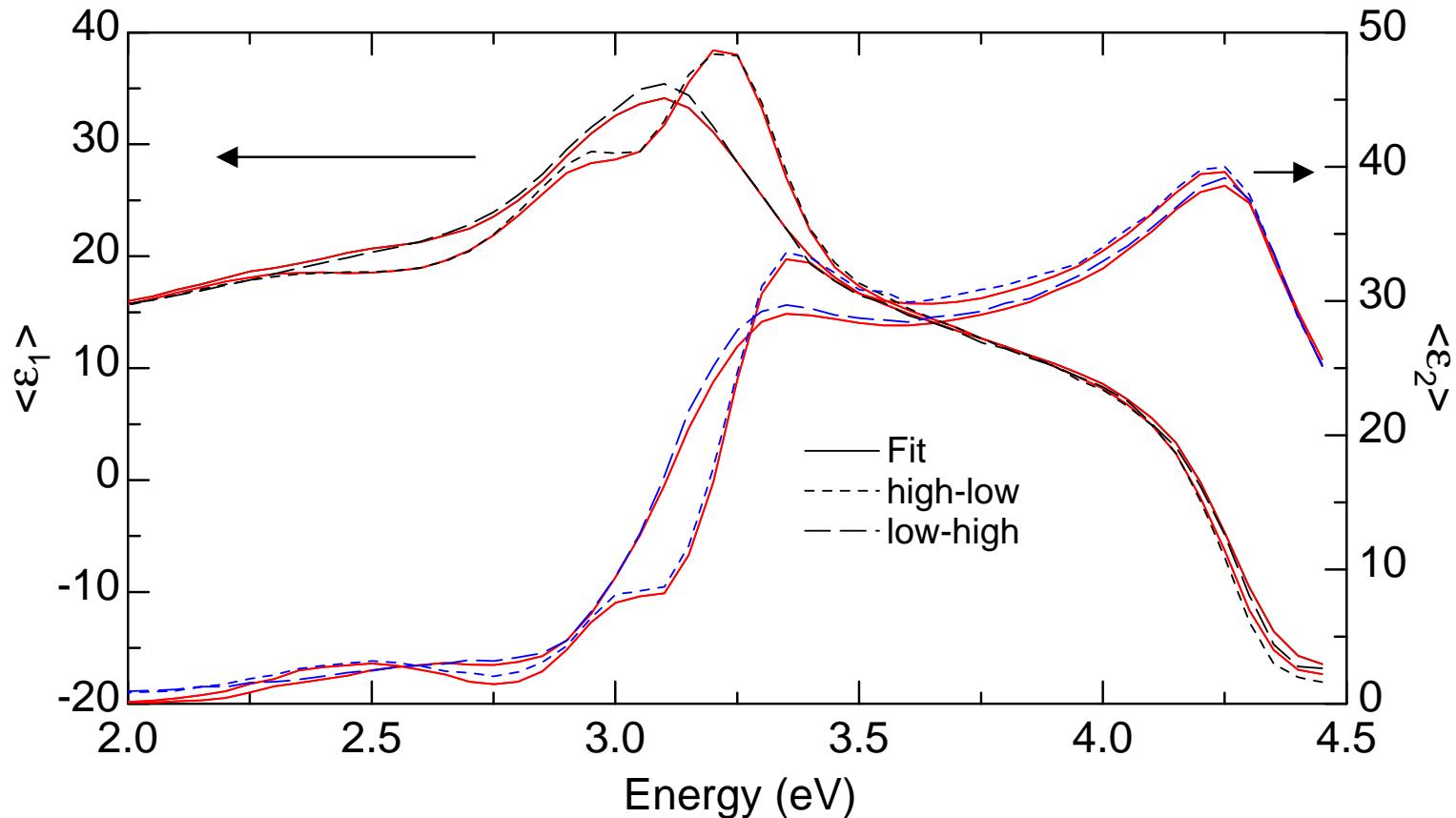
| | | |
|---|----------------|----------|
| 2 | sio2 | 9.9339 Å |
| 1 | sigewv x=0.110 | 6067.6 Å |
| 0 | si | 1 mm |

Calibration of DERA SiGe Epi Process

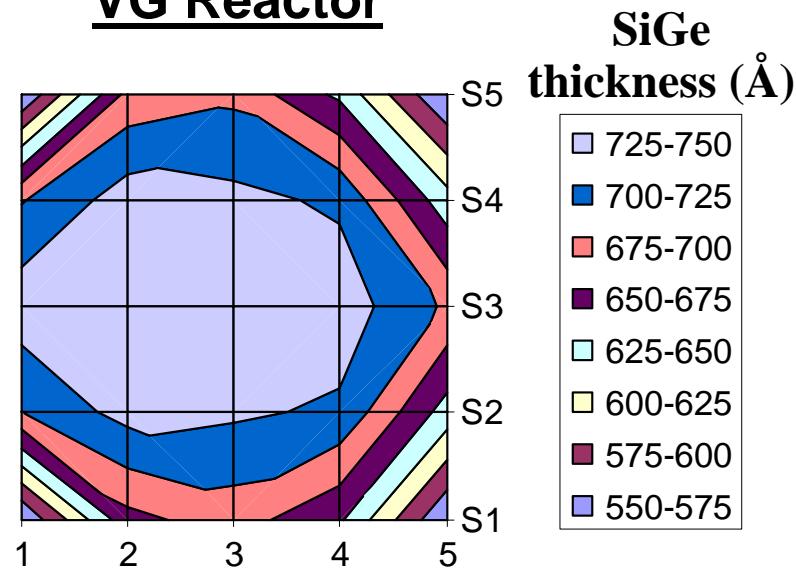


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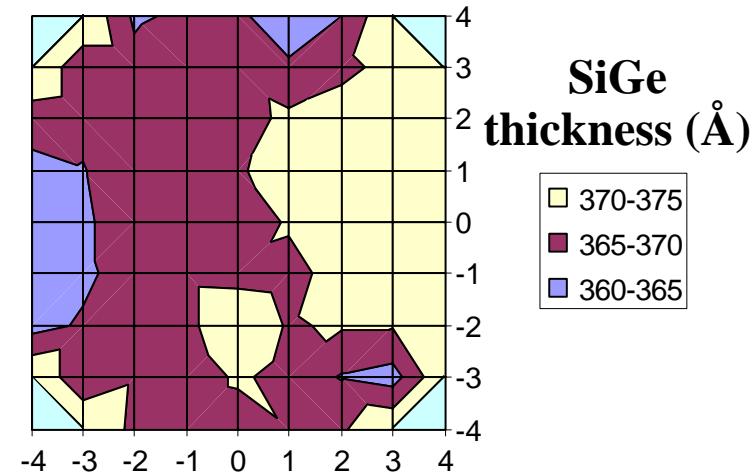
SE Characterisation of Graded SiGe Layers



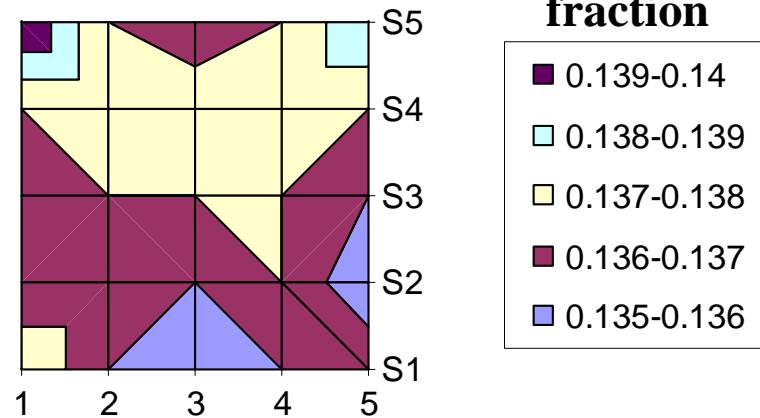
VG Reactor



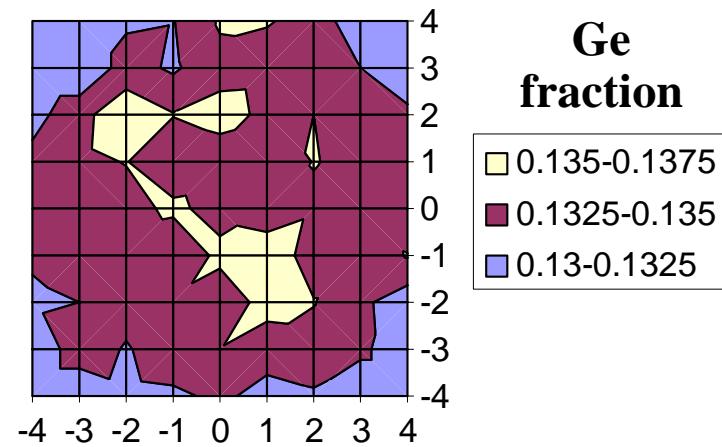
AMAT Reactor



Ge fraction

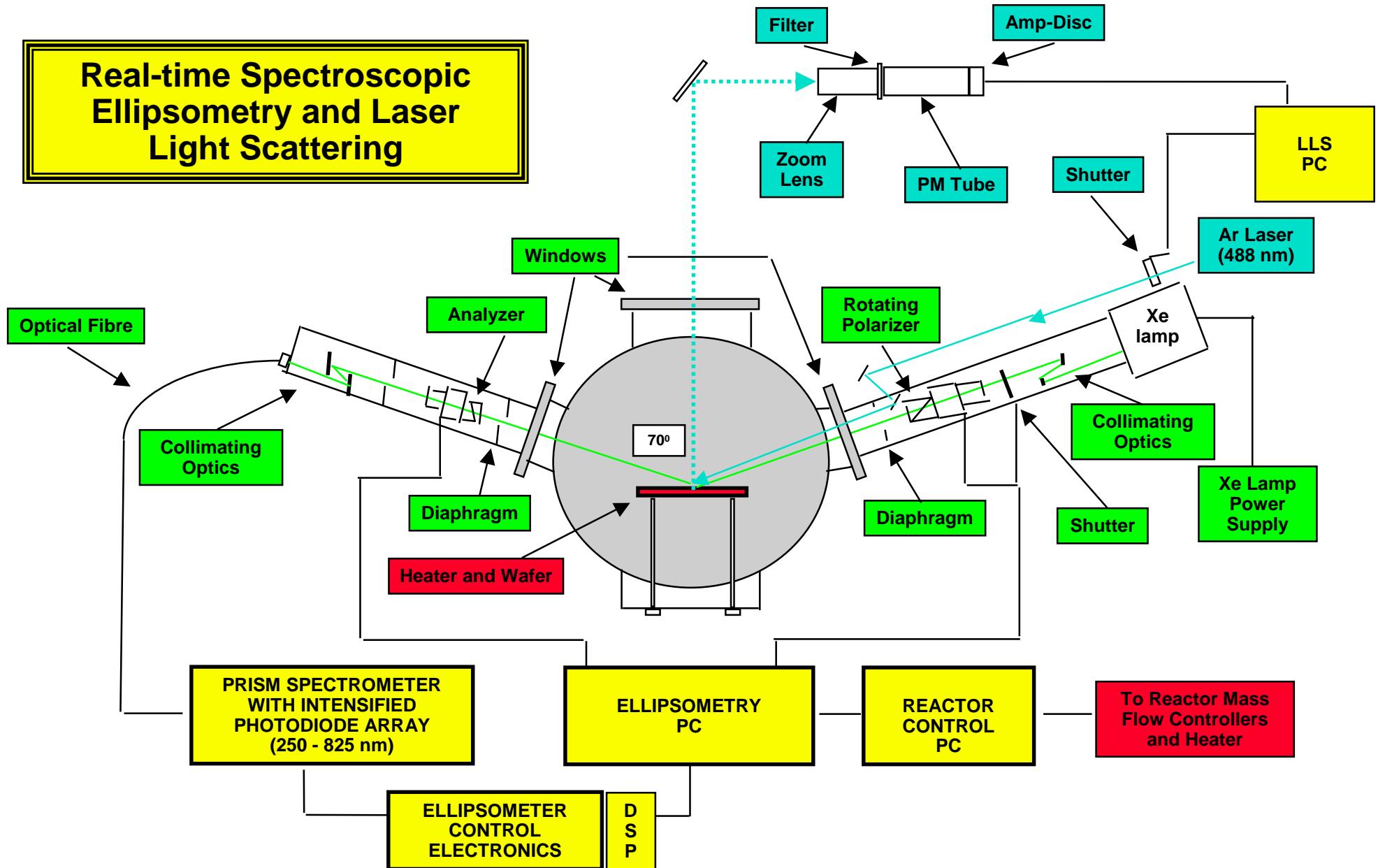


Ge fraction



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Real-time Spectroscopic Ellipsometry and Laser Light Scattering



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ANALYSIS OF REAL-TIME MULTI-WAVELENGTH DATA

■ Normal Spectral Regression

→ average x and total thickness

■ Virtual Interface Spectral Regression

→ surface x and growth rate as $f(\text{time})$ - demonstrated
→ accumulated thickness - endpointing demonstrated

■ Artificial Neural Networks (with Principal Components)

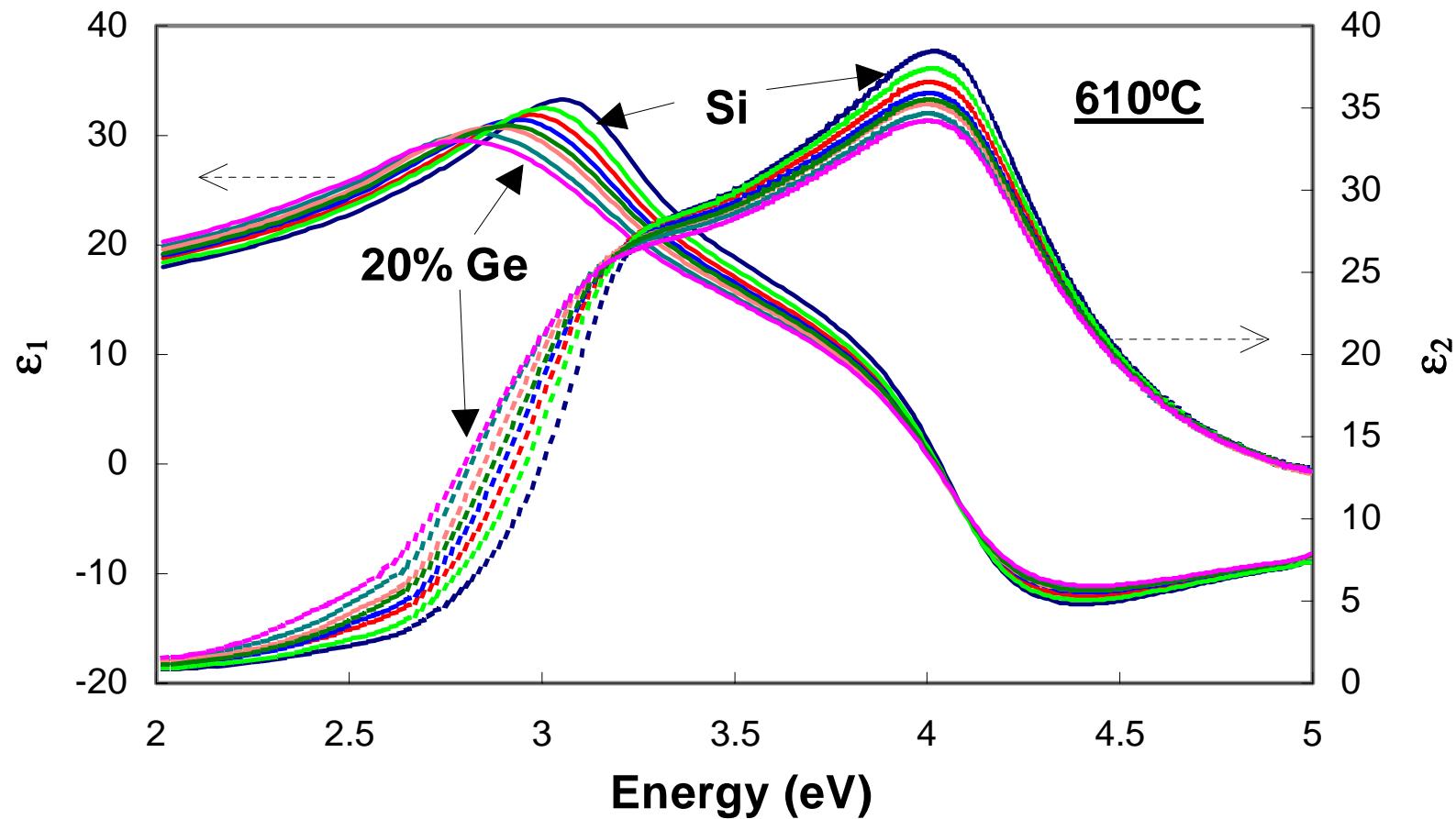
→ surface x as $f(\text{time})$ independent of growth rate - achieved
→ extensive training required for specific structures
→ very fast

■ Bayesian Statistical Tracking

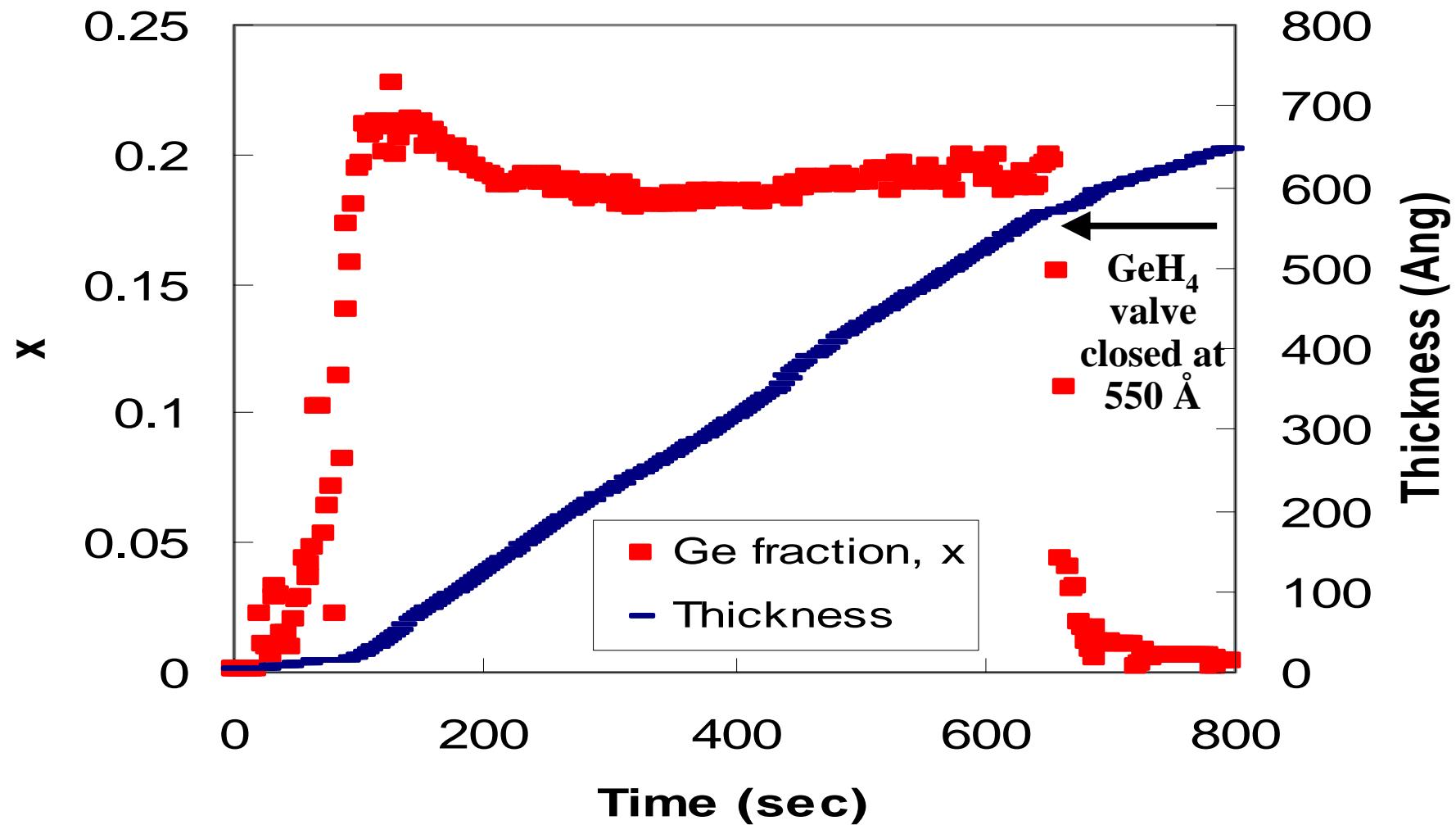
→ surface x and growth rate as $f(\text{time})$ - achieved
→ predictive tracking based on trends and system models
→ provides statistical confidence limits

SiGe High Temperature Reference Spectra

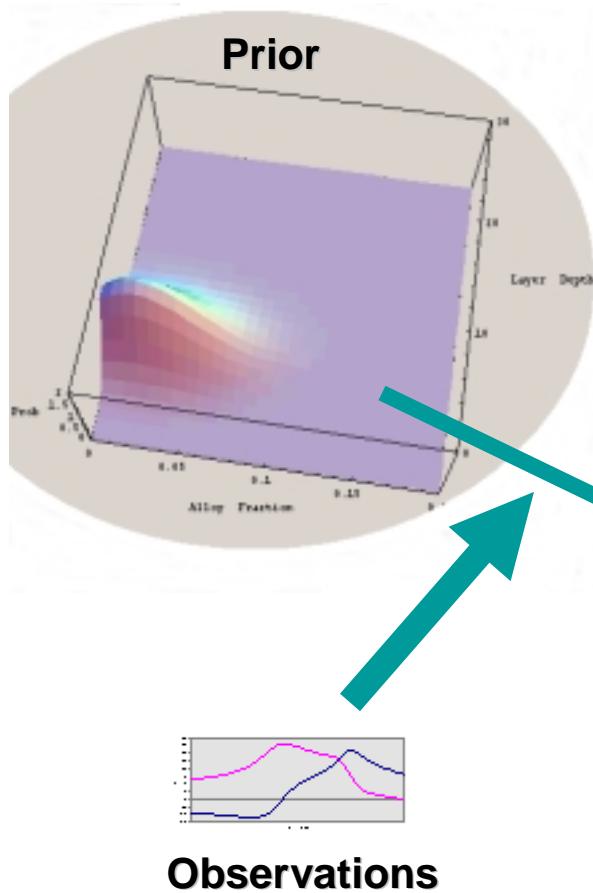
(0, 4, 6, 9, 11, 14, 17, 20% Ge)



Controlled Growth of 550 Å SiGe Layer

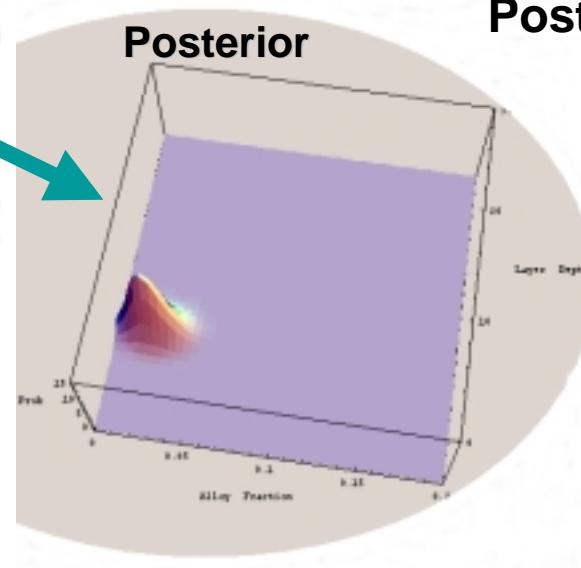


Sequential Bayesian estimation of composition



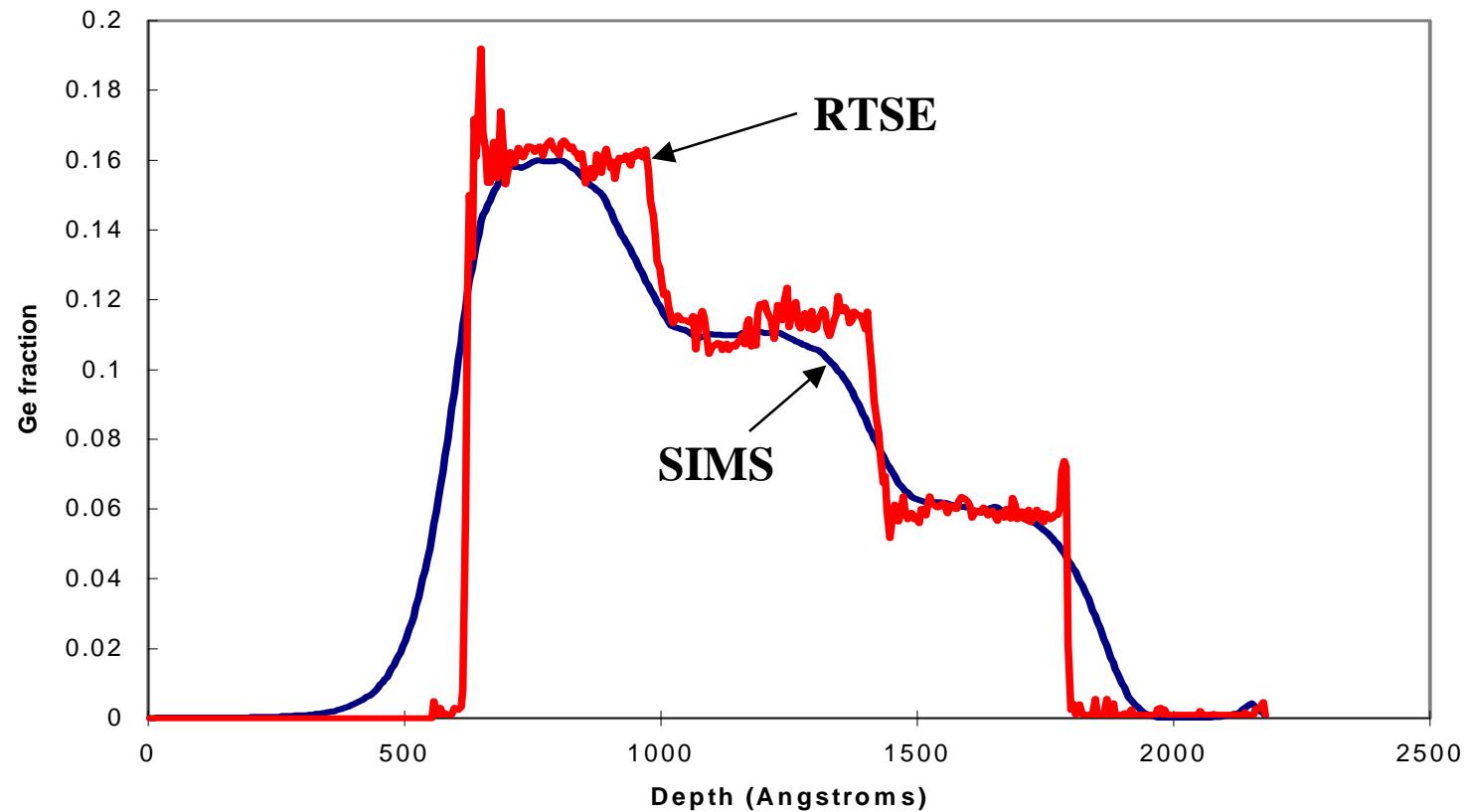
Bayesian approach to composition estimation using state of the art tracking methods:

Prior knowledge is updated to
Posterior knowledge in the
light of observations.

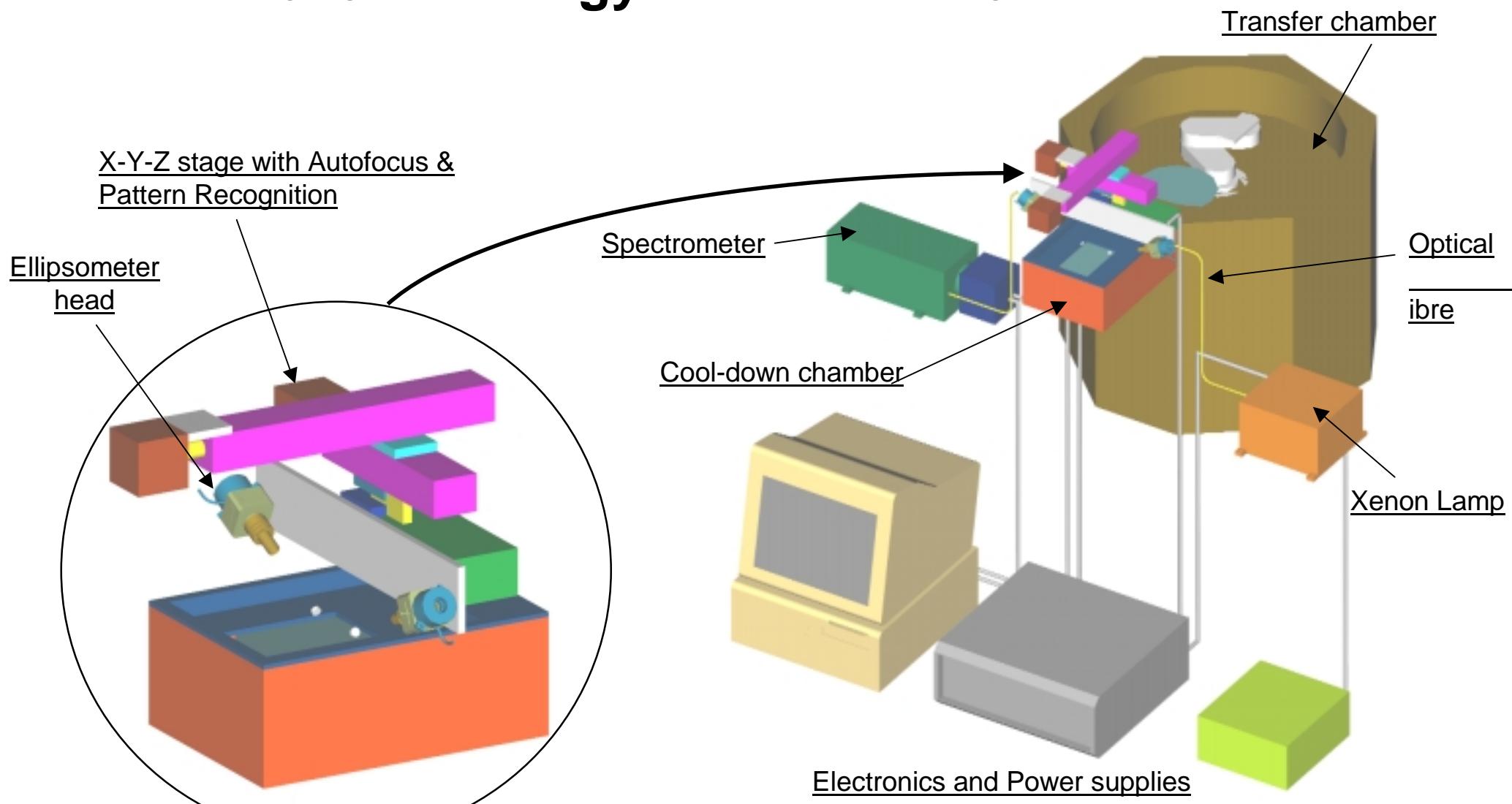


Incorporation of
system model to track
specified trajectory

Real-time SE and Off-line SIMS Data for Stepped Ge Layer Profile



MEDEA T618 Metrology Platform WP3.2



SOPRA

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Advantages of Integrated Metrology

- Rapid process development
- Evaluation of in-situ cleaning
- Run-to-run and chamber-to-chamber repeatability
- Cross-wafer uniformity
- Go/no go after each process step
- Accurate measurement of layers obscured by later processing, eg buried thin Si or SiGe, gradients
- Run-to-run control and early fault detection

Future Work

- Implementation of SE in cool-down chamber of AMAT Epi Centura
 - Adapt and test SE instrument
 - Reference spectra measurement
 - Methodology and system integration
- Evaluation of applicability
 - Measurements between processes and off-line verification
 - Tests on patterned wafers
- Recommendation for improved cost-effectiveness
 - Throughput, minimum requirements